

ONKYO® SERVICE MANUAL

MINIDISC RECORDER MODEL MD-185X



Silver model

UP/ UPT	230V AC,50Hz
D/UDT	120V AC,60Hz

SAFETY-RELATED COMPONENT WARNING!!

COMPONENTS IDENTIFIED BY MARK $\triangle$ ON THE SCHEMATIC DIAGRAM AND IN THE PARTS LIST ARE CRITICAL FOR RISK OF FIRE AND ELECTRIC SHOCK. REPLACE THESE COMPONENTS WITH ONKYO PARTS WHOSE PART NUMBERS APPEAR AS SHOWN IN THIS MANUAL.

MAKE LEAKAGE-CURRENT OR RESISTANCE MEASUREMENTS TO DETERMINE THAT EXPOSED PARTS ARE ACCEPTABLY INSULATED FROM THE SUPPLY CIRCUIT BEFORE RETURNING THE APPLIANCE TO THE CUSTOMER.

SPECIFICATIONS

System:	MiniDisc digital audio system
Recording system:	Magnetic field modulation overwrite system
Signal read out system:	Optical non-contact
Recording time:	Max. 74 min. (when using MDW-74)
Number of revolutions:	About 400 - 900 rpm (constant linear velocity)
Error correction system:	Advanced Cross Interleave Reed-solomon code
Sampling frequency:	44.1 kHz
Number of channels:	2 (stereo)
Frequency response:	10 Hz - 20 kHz ($\pm 0.5\text{dB}$)
Signal to noise ratio:	98 dB or more when playing
Output level:	1.3 volts r.m.s.
Power supply rating:	AC 230 V, 50 Hz AC 120 V, 60 Hz
Power consumption:	12W (AC230V, 50Hz) 14W (AC120V, 60Hz)
Dimensions (W $\times$ H $\times$ D):	185 $\times$ 131 $\times$ 298 mm (7-5/16" $\times$ 5-3/16" $\times$ 11-3/4")
Weight:	2.9 kg (6.4 lbs.)

Specifications and features are subject to change without notice.

ONKYO®
AUDIO COMPONENTS

CAUTION ON REPLACEMENT OF OPTIONAL PICKUP

The laser diode in the optical pickup block is sensitive to static electricity, surge current and etc. The components are liable to be damaged or its reliability remarkably deteriorated.

During repair, carefully take the following precautions. (The following precautions are included in the service parts.)

PRECAUTIONS

1. Ground for the work-desk.

Place a conductive sheet such as a sheet of copper (with impedance lower than 10Mohm) on the work-desk and place the set on the conductive sheet so that the chassis can be grounded.

2. Grounding for the test equipments and tools.

Test equipments and toolings should be grounded in order that their ground level is the same the ground of the power source.

3. Grounding for the human body.

Be sure to put on a wrist-strap that is properly grounded.

Be particularly careful when wearing synthetic fiber clothes, or air is dry.

4. Select a soldering iron that permits no leakage and have the tip of the iron well-grounded.

5. Do not check the laser diode terminals with the probe of a circuit tester or oscilloscope.

PROTECTION OF EYES FROM LASER BEAM DURING SERVICING

This set employs a laser. Therefore, be sure to carefully follow the instructions below when servicing.

WARNING !!

SERVICE WARNING: DO NOT APPROACH THE LASER EXIT WITH THE EYES TOO CLOSELY. IN CASE IT IS NECESSARY TO CONFIRM LASER BEAM EMISSION, BE SURE TO OBSERVE FROM A DISTANCE OF MORE THAN 30cm FROM THE SURFACE OF THE OBJECTIVE LENS ON THE OPTICAL PICK-UP BLOCK.

Laser Diode Properties

Material: GaAlAs
Wavelength: 780nm
Emission Duration: continuous
Laser output: max. 0.5mW*

* This output is the value measure at a distance about 1.8mm from the objective lens surface on the Optical pick-up Block.

LASER WARNING LABEL

These labels are located on the mechanism.

The label shown below are affixed.

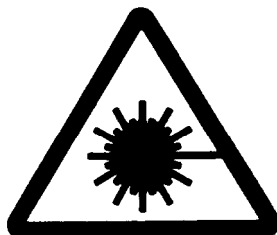
1. Warning label

This label is location on the chassis.

DANGER —INVISIBLE LASER RADIATION WHEN OPEN AND INTERLOCK FAILED OR DEFEATED. AVOID DIRECT EXPOSURE TO BEAM.

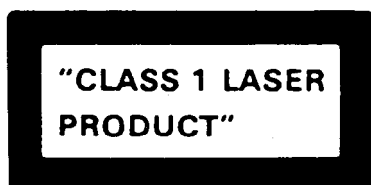
CAUTION —HAZARDOUS LASER AND ELECTROMAGNETIC RADIATION WHEN OPEN AND INTERLOCK DEFEATED

ATTENTION —RAYONNEMENT LASER ET ELECTROMAGNETIQUE DANGEREUX SI OUVERT AVEC L'ECLenchement DE SECURITE ANNULE.



2. Class 1 label

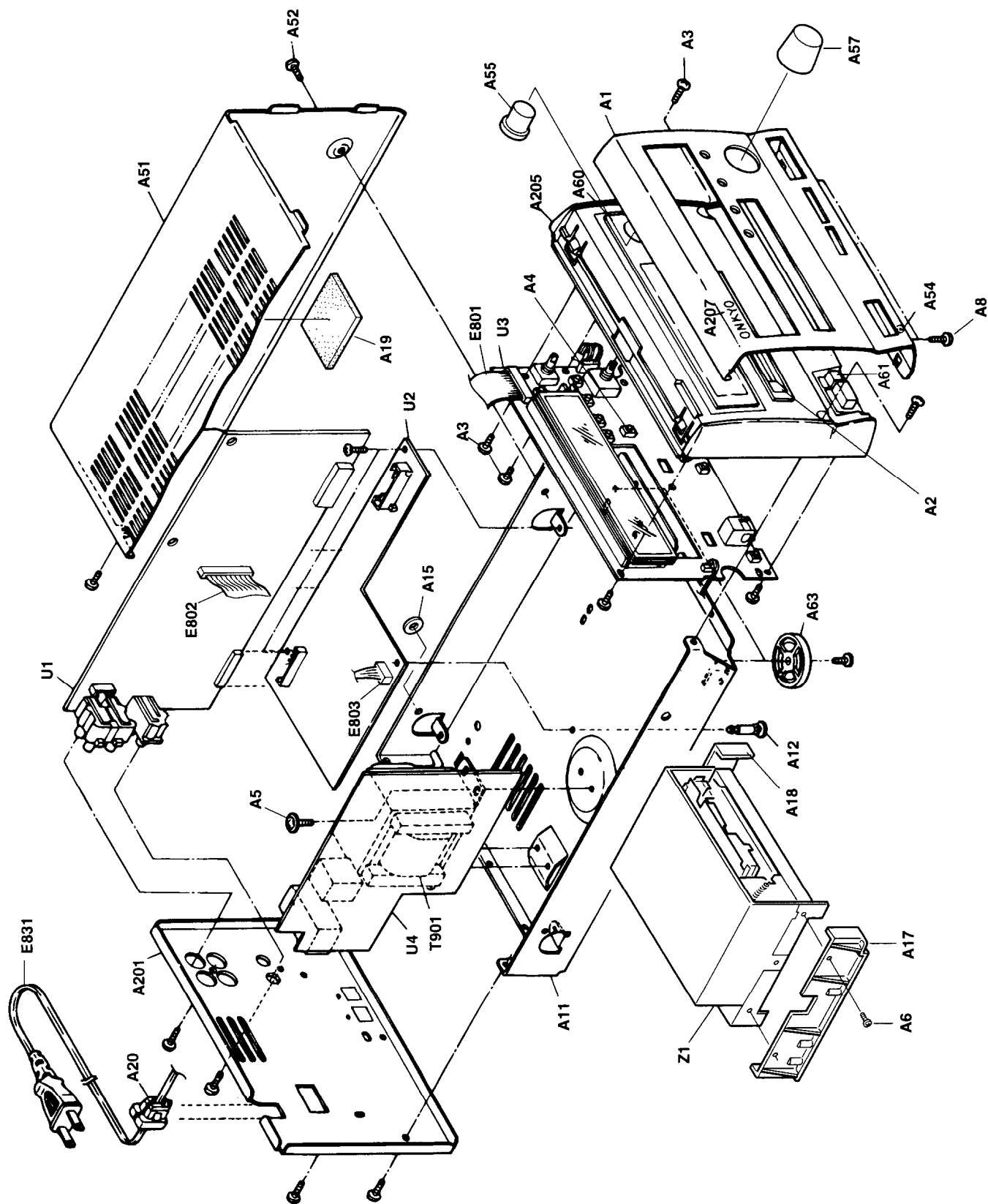
This label is located on the left side of rear cover.



LUOKAN 1
LASERLAITE

KLASS 1
LASER APPARAT

CHASSIS EXPLODED VIEW



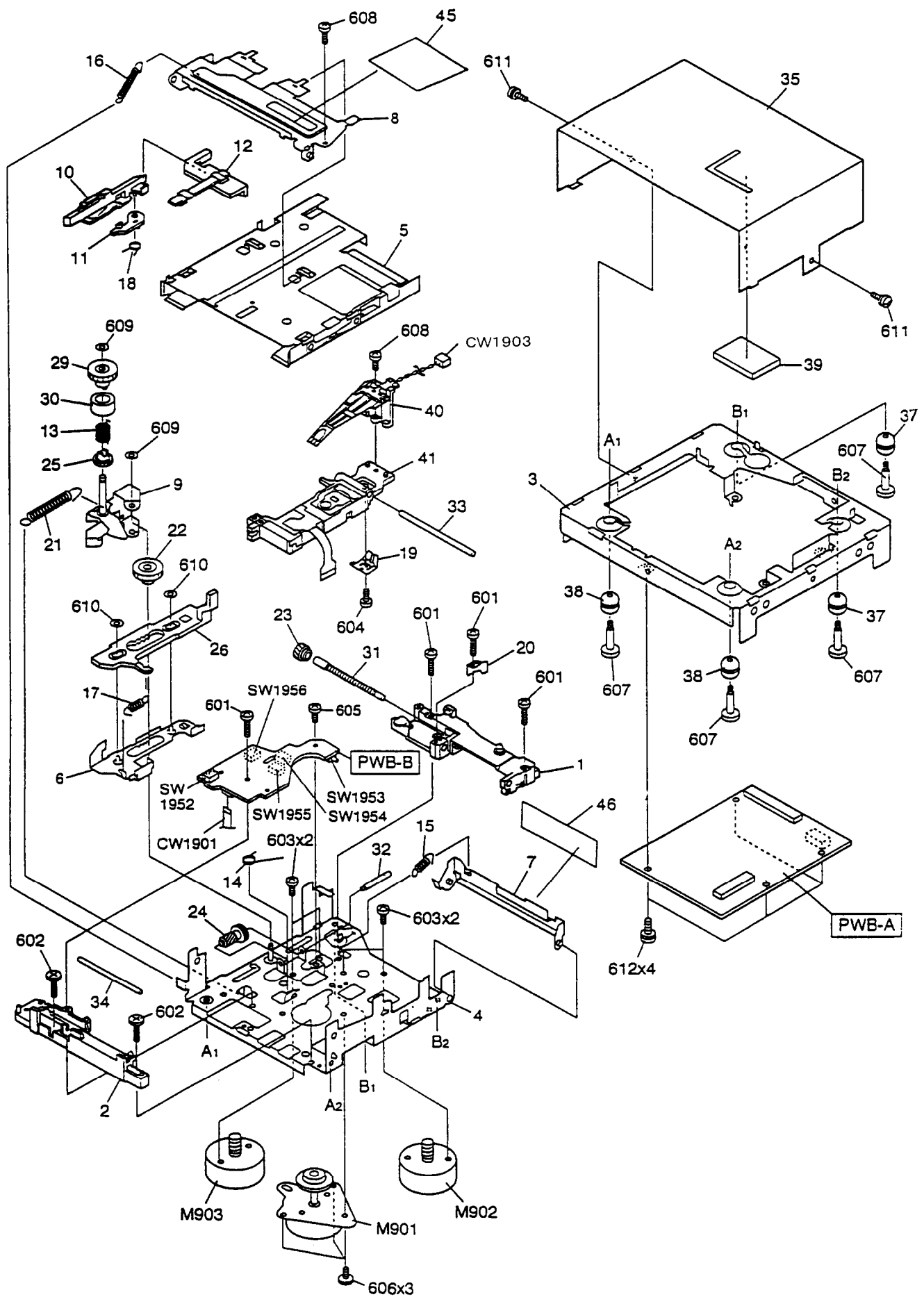
CHASSIS EXPLODED VIEW PARTS LIST

REF.NO.	PART NO.	DESCRIPTION	REF.NO.	PART NO.	DESCRIPTION
A1	27212028	Front panel	A205	27111086A	Front bracket
A2	28148404	Door	A207	28135247Y	Badge
A3	838130088	3TTB+8B,Self-tapping screw	E801	2045191522	NCFC5-191522,Flexible flat cable
A4	28325616	Knob, Open	E802	2045241512	NCFC5-241512,Flexible flat cable
A5	830440089	4TTC+8C(BC),Self-tapping screw	E803	2009990427	NSAS-8P0571,Socket
A6	838430107	3TTB+10S(BC),Self-tapping screw	E804	260208	Wire tie
A8	838430088	3TTB+8B(BC),Self-tapping screw	E831	253193HIT	△ AS-CEE,Power supply cord <P/PT>
A11	27100307B	Chassis	E831	253192HIT	△ AS-UC-6#18(SPT-2), Power supply cord <D/DT>
A12	27190266	KGLS-12RF,Holder	F901	252075	△ 2.5A-SE-EAK, Fuse <P/PT>
A13	27270393	Spacer	T901	2301353	△ NPT-1347P,Power transformer <P/PT>
A17	27191058	Holder ML	T901	2301352	△ NPT-1347D,Power transformer <D/DT>
A18	27191059	Holder MR	U1	1H389519-1B	NAAR-6419-1B,Main circuit PC board ass'y <P/PT>
A19	28141165	t2X40X40, Cushion	U1	1H389519-1C	NAAR-6419-1C,Main circuit PC board ass'y <D/DT>
A20	27300750	△ Bushing, Cord	U2	1H389520-1B	NAAR-6420-1B,Power supply circuit PC board ass'y <P/PT>
A51	28184626A	Top cover	U2	1H389520-1C	NAAR-6420-1C,Power supply circuit PC board ass'y <D/DT>
A52	838230088	3TTB+8B(NI), Self-tapping screw	U3	1H389521-1B	NADIS-6421-1B,Display circuit PC board ass'y <P/PT>
A54	28198831	Facet	U3	1H389521-1C	NADIS-6421-1C,Display circuit PC board ass'y <D/DT>
A54	28191712	Clear plate RE	U4	1H389522-1B	NAPS-6422-1B,Power transformer PC board ass'y <P/PT>
A55	28325614	Knob, recording	U4	1H389522-1C	NAPS-6422-1C,Power transformer PC board ass'y <D/DT>
A57	28325615	Knob, AMCS	U5	1H389523-1B	NAETC-6423-1B,AC outlet terminal PC board ass'y <P/PT>
A59	28133362	Back plate	Z1	24650029	MDM-97B
A60	28191835	Clear plate		27190970-1	Holder FA
A63	27175252-1B	Leg		29362285	Label, Caution
A201	27122500	Rear panel <P/PT>			
A201	27122499A	Rear panel <DT>			
A201	27122532	Rear panel <D>			

NOTE: <P>230V model only
 <D>120V model only
 <DT>Taiwanese model only
 <PT>Asian model only

NOTE: THE COMPONENTS IDENTIFIED BY MARK
 △ ARE CRITICAL FOR RISK OF FIRE AND
 ELECTRIC SHOCK. REPLACE ONLY WITH
 PART NUMBER SPECIFIED.

MD MECHANISM EXPLODED VIEW

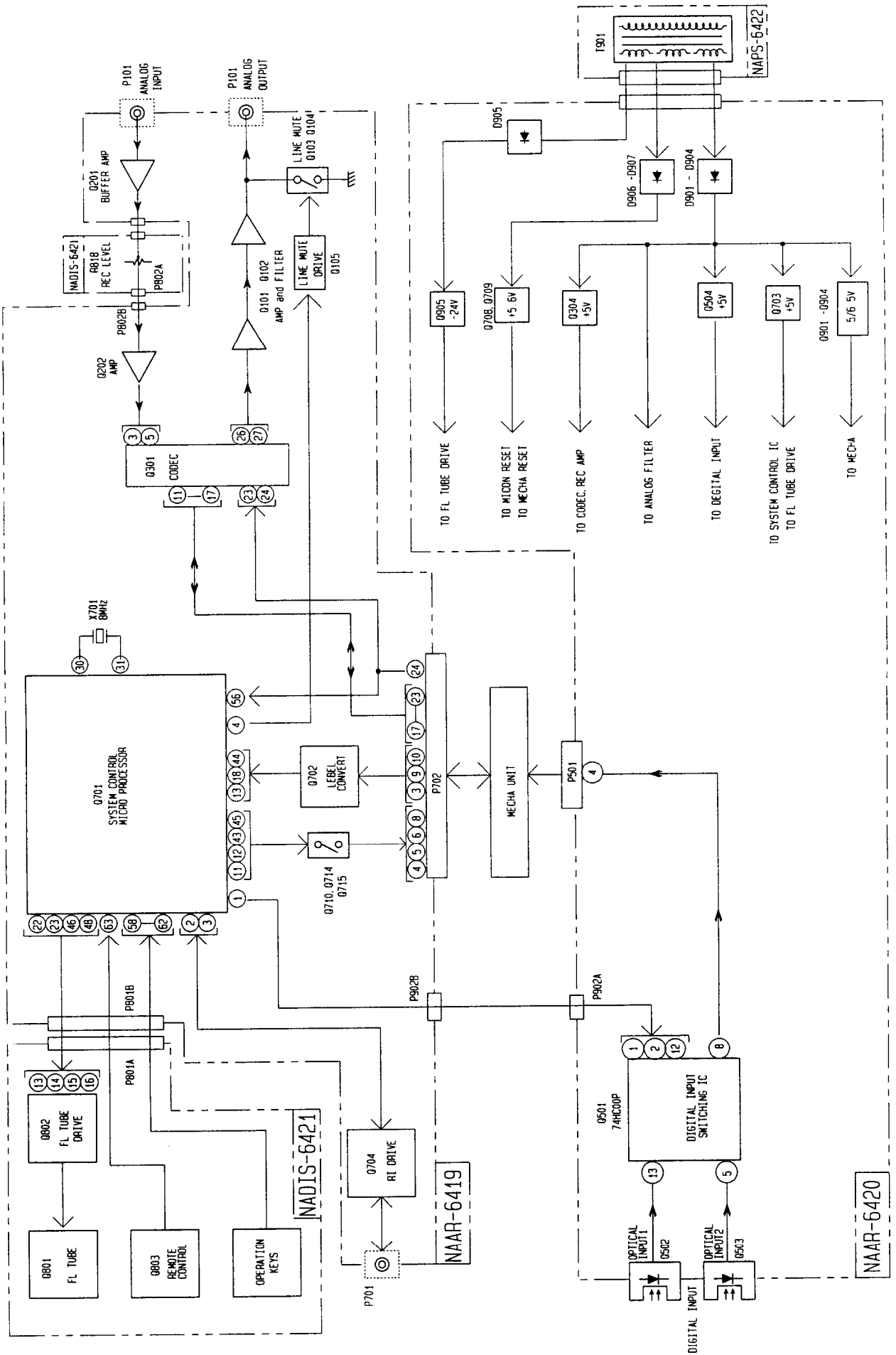


MD MECHANISM PARTS LIST

24650029(MDM-97B)

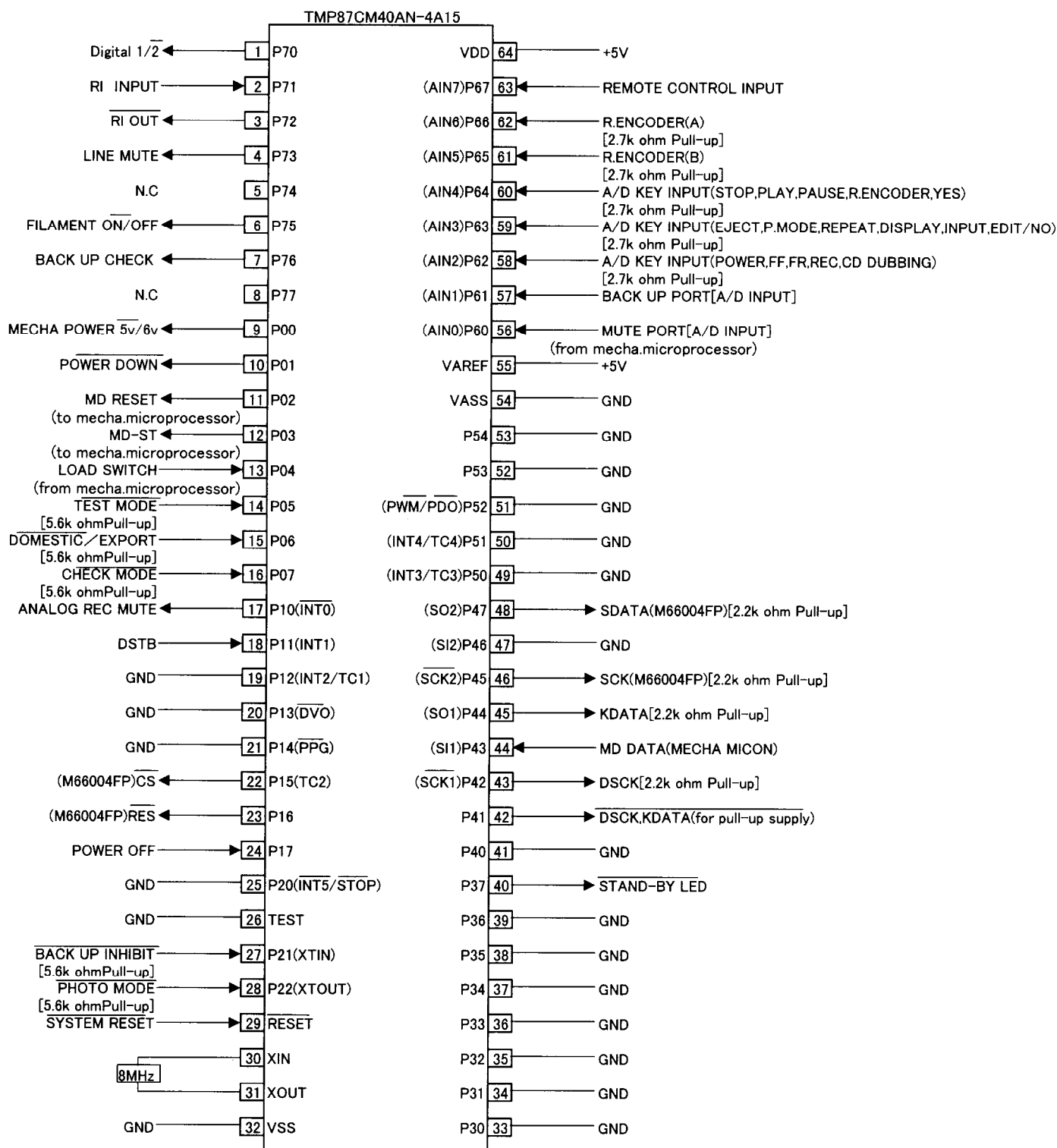
			Mechanism PC board-Parts list	
Ref.No.	Part No.	Description	CIRCUIT NO.	PART NO. DESCRIPTION
1	1242000208	MD guide A		Ics
2	1242000209	MD guide B	IC1101	1245730345 1R3R55
3	1242000210	Base frame	IC1201	1245730418 LR376481
4	1242070047	Driver chassis	IC1202	1245730207 IX2474AF
5	1242140138	Cartridge holder	IC1251	1245730043 74ACT02F
6	1242480122	Cam plate lever	IC1401	1245730480 IX0261AW
7	1242480123	Shift arm	IC1402	1245730301 S29294A
8	1242480124	Arm, holder	IC1601	1245730354 M56758FP
9	1242480125	Lever, roller arm	IC1801	1245730367 XC62EP32
10	1242480126	Lever, clamp	IC1906	1245730152 TC7ST08F
11	1242480127	Catcher	IC1907	1245730153 TC9246F
12	1242480128	Lever, slider	IC1915	1245730368 74VHC08FT
13	1242580157	Spring, roller holder	IC1990	1245730044 74AC04FS
14	1242580159	Spring		Transistors
15	1242580053	Spring	Q1251,Q1252	1245760042 2SK2909
16	1242580054	Spring, holder	Q1253,Q1254	1245760016 2SK1473
17	1242580055	Spring, rack	Q1401,Q1403	1245760032 RN2404
18	1242580094	Spring, catcher	Q1402,Q1404	1305760402 RNC1404
19	1242580160	Spring A	Q1451	1245760031 RNC1407
20	1242580161	Spring, drive shaft	Q1601,Q1801	1245760001 2SA1314C
21	1242580093	Spring, roller holder	Q1802,Q1803	1245760047 RN1406
22	1242810063	Loading gear B	Q1807	1245760001 2SA1314C
23	1242810064	Drive gear	Q1820	1305760552 2SA1162G
24	1242810065	Loading gear A	Q1821,Q1822	1245760031 RNC1407
25	1242810066	Gear, roller		Diodes
26	1242810067	Gear, rack	D1251,D1252	1245700005 SB02-09CP
29	1242860001	Holder, roller	D1999	1245700008 1SS372
30	1242870014	Roller, transfer		Coils
31	1242900105	Shaft, drive	L1101	1245850024 10 μ H
32	1242900108	Shaft, loading gear	L1102,L1201	1245850026 0.47 μ H
33	1242900039	Shaft,pickup guide	L1203	1245850025 4.7 μ H
34	1242900040	Shaft,pickup guide	L1251	1245850002 47 μ H
35	1242000225	Cover, top	L1601,L1950	1246140023 1 μ H
37	1243260020	Cushion A		Crystal
38	1243260021	Cushion B	XL1201	1246160018 33.8688MHz
39	1243260108	Cushion, head		Plugs
40	1246100008	Head	CN1101	1245100161 23P
41	1246170019	Optical pickup	CN1252	1425100204 2P
42	1245120301	MD flexible cable	CN1601	1245100162 5P
43	1245120299	Connector	CN1602	1245100163 2P
45	1244030163	Sheet, head	CN1603	1425100236 2P
46	1244030168	Sheet lead wire	CN1604	1245100164 2P
601	1249700161	1.7x9.5mm,Machine screw	CN1901	1245100160 5P
602	1249700162	1.7x7.5mm,Machine screw	CN1902	1245100165 24P
603	1249700163	1.7x2mm,Machine screw	CN1904	1425100223 4P
604	1249700011	M1.4*2.2,Machine screw		Cable
605	1249700061	M1.7*3,Machine screw	CW1901	1245120287
606	1249700065	M1.7*2.5,Machine screw		Connector
607	1249700076	M1.7*8.9,Machine screw	CW1903	1245120288
608	1249700108	M1.7*5,Machine screw		
609	1249900005	1.5W3.2-.5,Washer		
610	1249900006	1.2W3-0.25,Waseh		
611	1189700020	M2.0*4,Machine screw		
612	1249700077	M1.7*3,Machine screw		
PWB-A	1246840193	Main PC board ass'y		
PWB-B	1245210149	Mechanism PC		
M901	1246300048	Spindle motor		
M902	1246300049	Sled motor		
M903	1246300050	Loading motor		
SW1954,SW1955	1245300026	Push switch		
SW1952	1245300027	Push switch		
SW1956	1305301313	Loading switch		
SW1953	1305301315	Lead-in switch		

BLOCK DIAGRAM



MICROPROCESSOR TERMINAL DESCRIPTION

Q701:TMP87CM40AN-4A15 (SYSTEM CONTROL MICROPROCESSOR)

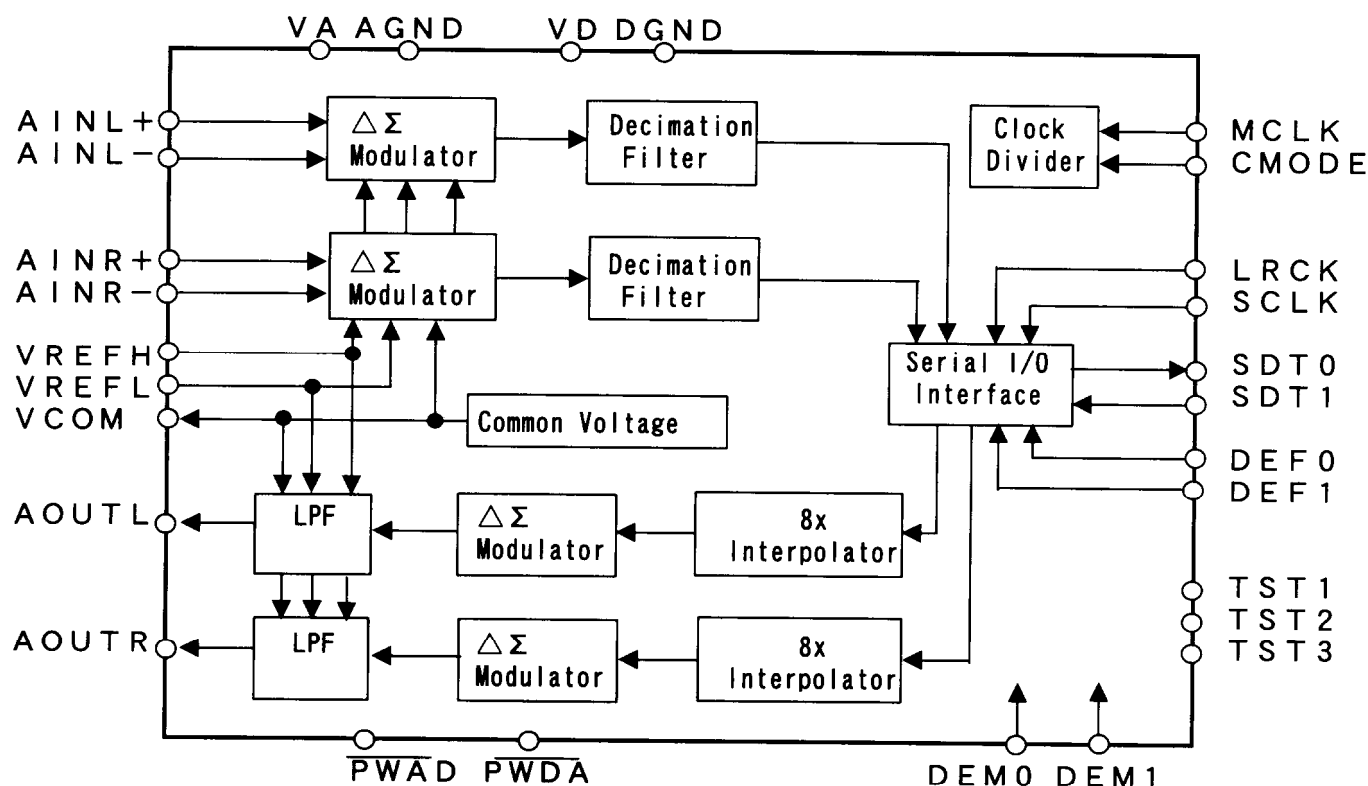


Q701:TMP87CM40AN-4A15

PIN No.	SYMBOL	DESCRIPTION
1	DIGITAL 1/2	Digital input select signal output terminal
2	RI INPUT	RI signal input terminal
3	RI OUT	RI signal output terminal(Active low)
4	LINE MUTE	Signal output terminal for LINE MUTE control. (Active high)
5	NOT USED	N.C
6	FILAMENT	Filament control signal output terminal(Active low)
7	BACKUP CHECK	Demand signal output terminal for backup voltage confirmation.
8	NOT USED	N.C
9	MECHA POWER	Mechanism drive voltage switch signal output terminal.
10	POWER DOWN	Signal output terminal for power failure detection(active low)
11	MD RESET	Reset signal output terminal to mecha microprocessor
12	MD-ST	Command signal output terminal for mecha microprocessor
13	LOAD SWITCH	Signal input terminal for lording switch
14	TEST MODE	Test mode detect input terminal (active low)
15	DOMESTIC/EXPORT	Dome./Ex-area detect input terminal
16	CHECK MODE	Check mode input terminal
17	ANALOG REC MUTE	Analogue recording mute output terminal
18	DSTB	Communication input terminal for mech.microprocessor
19	NOT USED	N.C (to GND)
20	NOT USED	N.C (to GND)
21	NOT USED	N.C (to GND)
22	CS	Chip selection signal output terminal to FL driver(M66004FP)
23	RES	Reset signal output terminal to FL driver(M66004FP) active low
24	POWER OFF	Detection signal input terminal for power failure
25	NOT USED	N.C (to GND)
26	TEST	Test terminal for microprocessor (connect to GND)
27	BACKUP INHIBIT	Back up suppression input terminal
28	PHOTO MODE	Input terminal for taking a picture mode detection.
29	SYSTEM RESET	System reset signal input terminal
30	XIN	connect to clock(8MHz)
31	XOUT	connect to clock(8MHz)
32	Vss	Power supply terminal (to GND)
33	NOT USED	N.C (to GND)
34	NOT USED	N.C (to GND)
35	NOT USED	N.C (to GND)
36	NOT USED	N.C (to GND)
37	NOT USED	N.C (to GND)
38	NOT USED	N.C (to GND)
39	NOT USED	N.C (to GND)
40	STAND-BY LED	Output terminal for stand-by LED control
41	NOT USED	N.C (to GND)
42	PULL UP VCC	Signal output terminal (voltage control) for DSCK,KDATA
43	DSCK	Serial communication terminal to mecha microprocessor(output terminal)
44	MD DATA	Serial communication terminal to mecha microprocessor(input terminal)
45	KDATA	Serial communication terminal to mecha microprocessor(output terminal)
46	SCK	Clock output terminal for serial communications to FL driver(M66004FP)
47	NOT USED	N.C (to GND)
48	SDATA	Data output terminal for serial communications to FL driver(M66004FP)
49	NOT USED	N.C (to GND)
50	NOT USED	N.C (to GND)
51	NOT USED	N.C (to GND)
52	NOT USED	N.C (to GND)
53	NOT USED	N.C (to GND)
54	VASS	Reference voltage terminal for A/D transfer(to GND)
55	VAREF	Reference voltage terminal for A/D transfer(to +5V)
56	MUTE PORT	A/D input terminal for mute control (Active high)
57	BACK UP PORT	A/D input terminal for backup voltage
58	KEY INPUT(1)	A/D key input terminal (1)
59	KEY INPUT(2)	A/D key input terminal (2)
60	KEY INPUT(3)	A/D key input terminal (3)
61	R.ENCODER(B)	Pulse input terminal for rotary-encoder
62	R.ENCODER(A)	Pulse input terminal for rotary-encoder
63	REMOCON	Signal input terminal for remote control
64	VDD	Power supply terminal (to +5V)

IC BLOCK AND DESCRIPTIONS

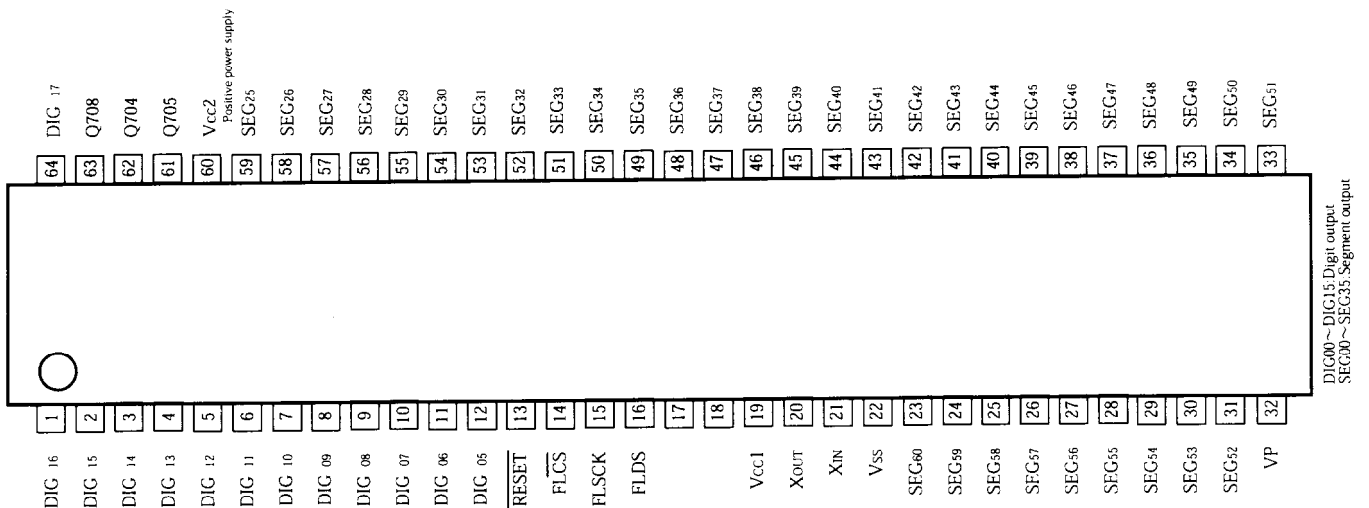
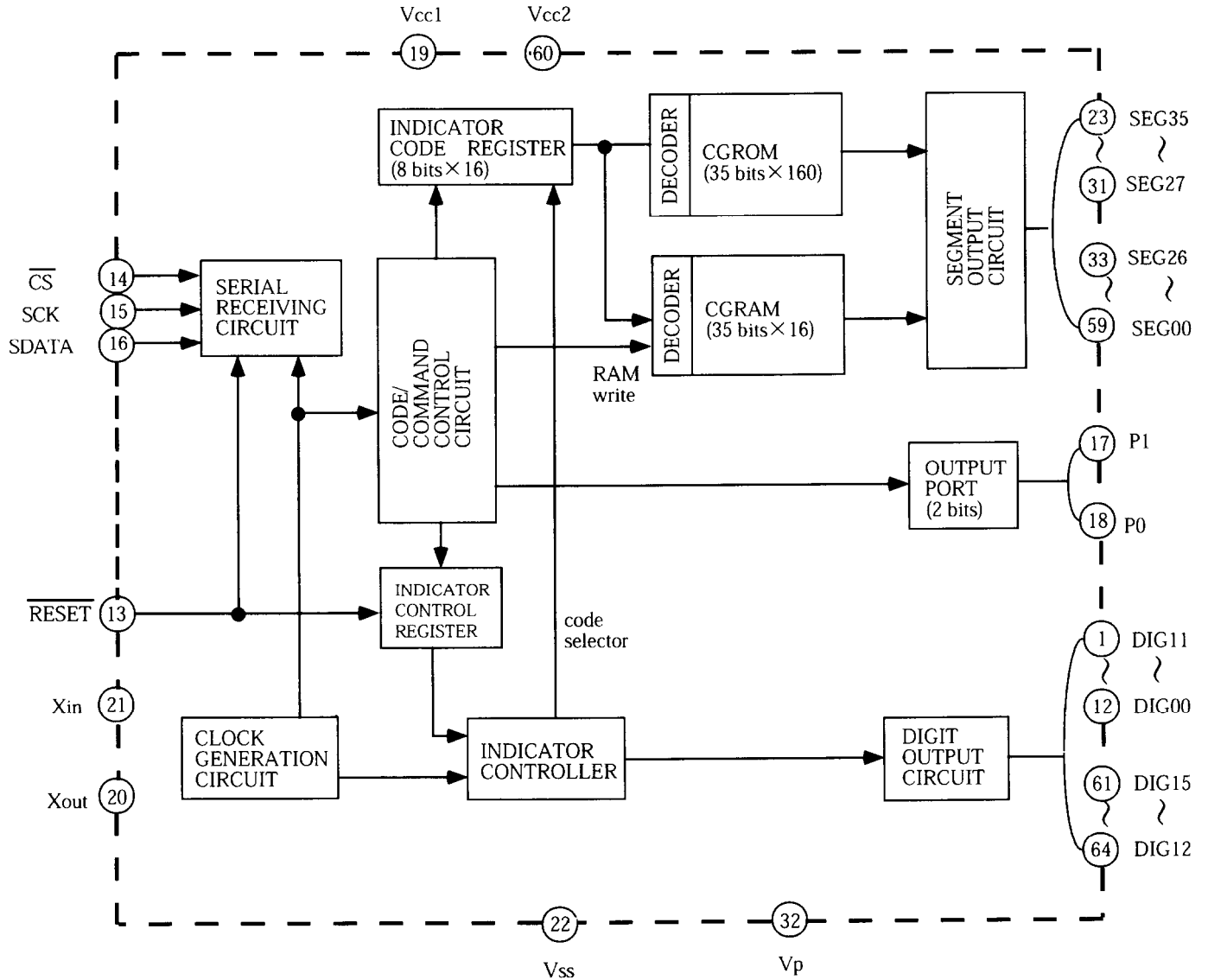
Q301:AK4520A (A/D, D/A CONVERTER)



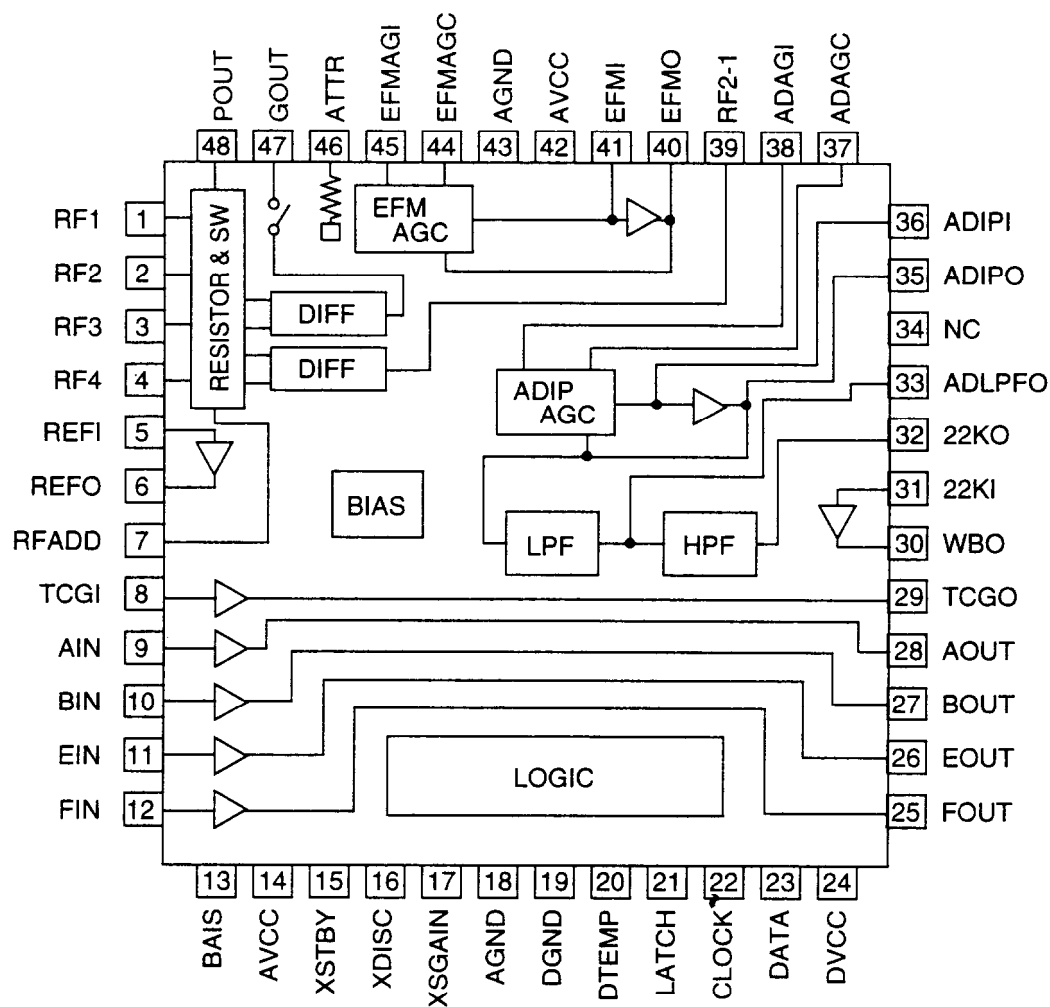
PIN No.	SYMBOL	DESCRIPTION
1	VREFH	Positive Voltage Reference Input Pin, VA
2	VREFL	Negative Voltage Reference Input Pin, AGND
3	AINR+	Rch Analog Positive Input Pin
4	AINR-	Rch Negative Positive Input Pin
5	AINL+	Lch Analog Positive Input Pin
6	AINL-	Lch Negative Positive Input Pin
7	VA	Analog Power Supply Pin
8	AGND	Analog Ground Pin
9	DIF0	Audio Data Interface Format Pin
10	DIF1	Audio Data Interface Format Pin
11	LRCK	Input/Output Channel Clock Pin
12	SCLK	Audio Serial Data Clock Pin
13	SDTI	Audio Serial Data Input Pin
14	SDTO	Audio Serial Data Output Pin
15	MCLK	Master Clock Input Pin
16	DEM0	De-emphasis Frequency Select Pin
17	DEM1	De-emphasis Frequency Select Pin
18	TST3	Test Pin
19	TST2	Test Pin
20	TST1	Test Pin
21	VD	Digital Power supply Pin
22	DGND	Digital Ground Pin
23	PWDA	DAC Power-Down Mode Pin
24	PWAD	ADC Power-Down Mode Pin
25	CMODE	Master Clock Select Pin "H":384fs,"L":256fs
26	AOUTL	Lch Analog Output Pin
27	AOUTR	Rch Analog Output Pin
28	VCOM	Common Voltage Output Pin VA/2

1	VREFH	MCLK	28
2	VREFL	DEMO	27
3	AINR+	DEM1	26
4	AINR-	TST3	25
5	AINL+	TST2	24
6	AINL-	TST1	23
7	VA	AKM AK 4 5 2 0 AVF VD	22
8	AGND	DGND	21
9	DIF0	PWDA	20
10	DIF1	PWAD	19
11	LRCK	CMODE	18
12	SCLK	AOUTL	17
13	SDTI	AOUTR	16
14	SDTO	VCOM	15

Q802:M66004FP (FL TUBE DRIVER)



IC1101:IR3R55 (RF SIGNAL PROCESSOR AMP)

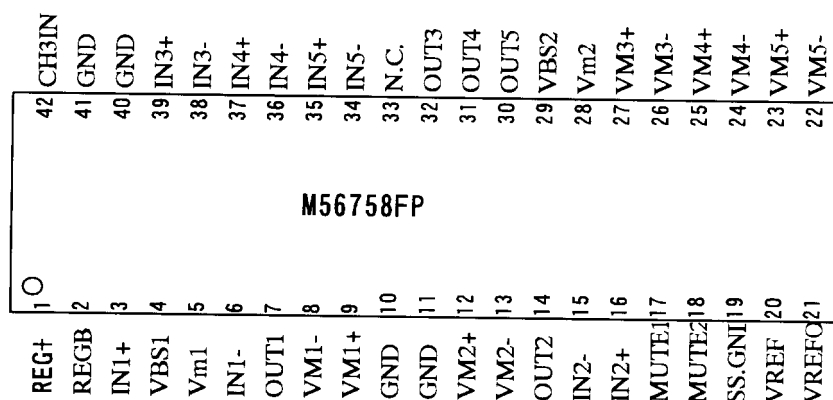


IC1101:IR3R55 (RF AMP)

PIN No.	SYMBOL	DESCRIPTION
1	RF1	RF signal input port1(to pick up)
2	RF2	RF signal input port2(to pick up)
3	RF3	RF signal input port3(to pick up)
4	RF4	RF signal input port4(to pick up)
5	REF1	Reference voltage (input)
6	REF0	Reference voltage (output)
7	RFADD	Add output ports of RF1 to RF4
8	TCGI	Track cross detect input port
9	AIN	Focus-servo signal amplifire
10	BIN	Focus-servo signal amplifire
11	EIN	Tracking-servo signal amplifire
12	FIN	Tracking-servo signal amplifire
13	BIAS	Bias signal input port
14	AVCC	Power supply for analogue part
15*	VSTBY	Logic-signal output(STBY signal)
16*	XDISC	Logic-signal output(DISC signal)
17*	XSGAIN	Logic-signal output(SGAIN signal)
18	AGND	GND port (analogue part)
19	DGND	GND port (digital part)
20	DTEMP	Detect a chip-emperature
21	LATCH	Latch signal input
22	CLOCK	Clock signal input
23	DATA	Serial data input
24	DVCC	Power supply for digital ports
25	FOUT	Output signal port for tracking servo
26	EOUT	Output signal port for tracking servo
27	BOUT	Output signal port for focus servo
28	AOUT	Output signal port for focus servo
29	TCGO	Track cross detect output port
30	WBO	Comparetor output port (ADIP signal)
31	22KI	Comparetor input port (ADIP signal)
32	22KO	HPF signal output port (ADIP signal)
33	ADLPFO	LPF signal output port (ADIP signal)
34*	NC	NC
35	ADIPO	Pre-amp output port (ADIP signal)
36	ADIPI	AGC-amp output port (ADIP signal)
37	ADAGC	Conect a across-capacitor for AGC (ADIP signal)
38	ADAGI	AGC-amp input port (ADIP signal)
39	RF2-1	Difference signal of RF1 to RF2
40	EFMO	Pre-amp output port (RF signal)
41*	EFMI	AGC-amp output port (RF signal)
42	AVCC	Power supply of analogue part
43	AGND	GND port of analogue part
44	EFMAGC	AGC capacitor connect port (EFM signal)
45	EFMAGI	AGC-amp input port (EFM signal)
46*	ATTR	Output signal adjust port (47 and 48pin)
47	GOUT	RF1+RF2+RF3+RF4 signals output
48	POUT	RF1 to RF4 output

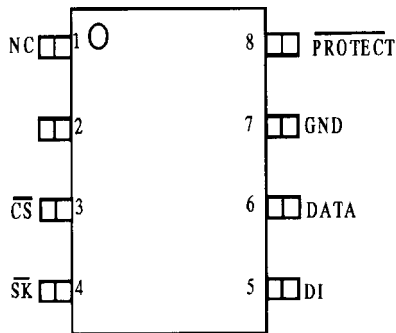
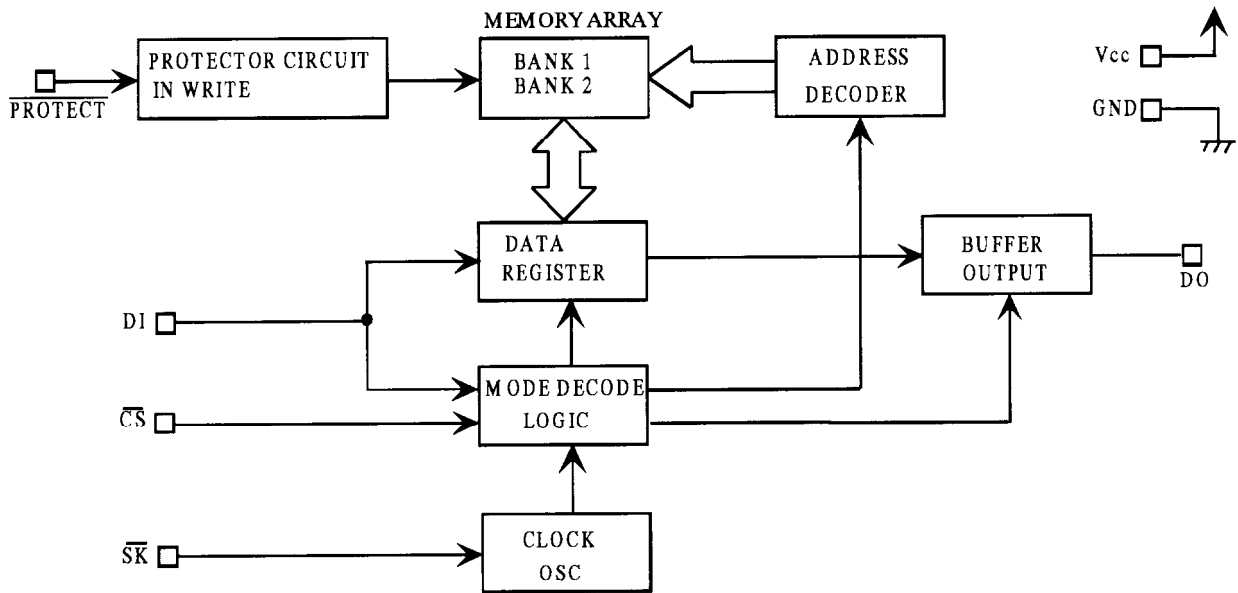
*This mark terminals is open port.

IC1601:M56758FP (5 CHANNEL ACTUATOR DRIVER IC)



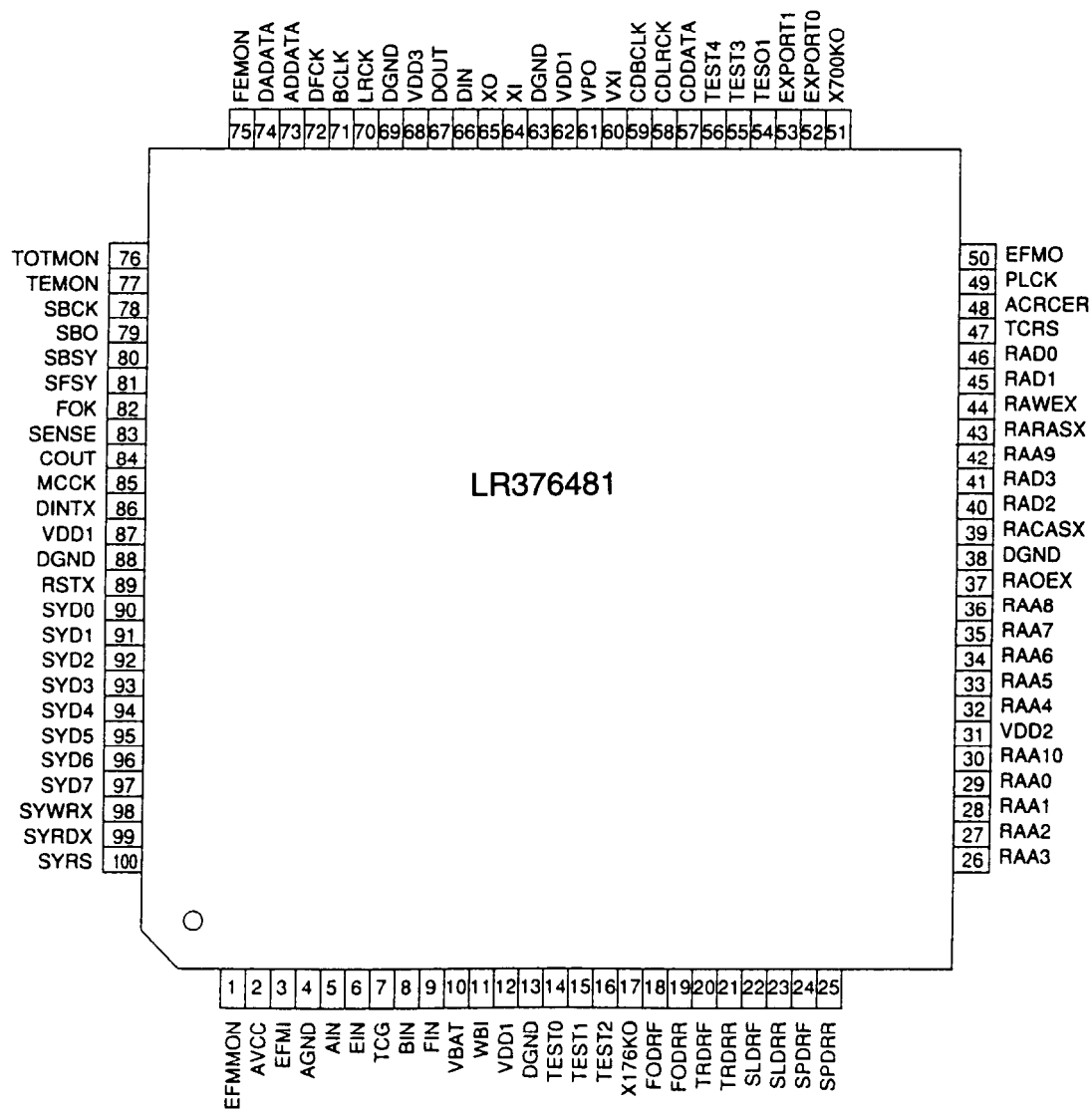
PIN No.	SYMBOL	DESCRIPTION	PIN No.	SYMBOL	DESCRIPTION
1	REG+	Voltage setting port for regulator	42	CH3IN	Non-inverted input port of CH3
2	REGB	Connect base position for regulator	41	OUT3	Output port of E3-amp.
3	IN1+	Non-inverted input port of E1-amp.	40	IN3-	Inverted input port of E3-amp.
4	VBS1	Power supply for boost-trap	39	VBS2	Power supply for boost-trap
5	Vm1	Power supply of motor	38	Vm2	Power supply of motor
6	IN1-	Inverted input port of E1-amp.	37	IN3+	Non-inverted input port of CH3
7	OUT1	Output port of E1-amp.	36	N.C.	N.C
8	VM1-	Inverted output port of CH1	35	VM3-	Inverted output port of CH3
9	VM1+	Non-inverted output port of CH1	34	VM3+	Non-inverted output port of CH3
10	GND	Ground(motor)	33	GND	Ground(motor)
11	GND	Ground(motor)	32	GND	Ground(motor)
12	VM2+	Non-inverted output port of CH2	31	VM4+	Non-inverted output port of CH4
13	VM2-	Inverted output port of CH2	30	VM4-	Inverted output port of CH4
14	OUT2	Output port of E2-amp.	29	VM5+	Non-inverted output port of CH5
15	IN2-	Inverted input port of E2-amp.	28	VM5-	Inverted output port of CH5
16	IN2+	Non-inverted input port of E2-amp.	27	OUT5	Output port of E5-amp.
17	MUTE1	Mute ports of CH1 to CH4	26	IN5-	Inverted input port of E5-amp.
18	MUTE2	Mute port of CH5	25	IN5+	Non-inverted input port of E5-amp.
19	SS.GND	Ground(signal)	24	IN4+	Non-inverted input port of E4-amp.
20	VREF	Input port of voltage reference	23	IN4-	Inverted input port of E4-amp.
21	VREFO	Output port of voltage reference	22	OUT4	Output port of E4-amp.

IC1402:S29294A (EEP-ROM) PROGRAMMABLE ROM



PIN	PIN No.	DESCRIPTION
$\overline{CS}$	3	Chip select(Input)
$\overline{SK}$	4	Serial clock(input)
DI	5	Serial data(input)
DATA	6	Serial data(output)
GND	7	Ground
PROTECT	8	Protect controlled of memory(input) Connect GND or open :Effective protect To Vcc :Ineffective protect
NC	1	Open
Vcc	2	Supply voltage

IC1201:LR376481 (ENCODER/DECODER)



IC1201:LR376481 (ENCODER/DECODER)

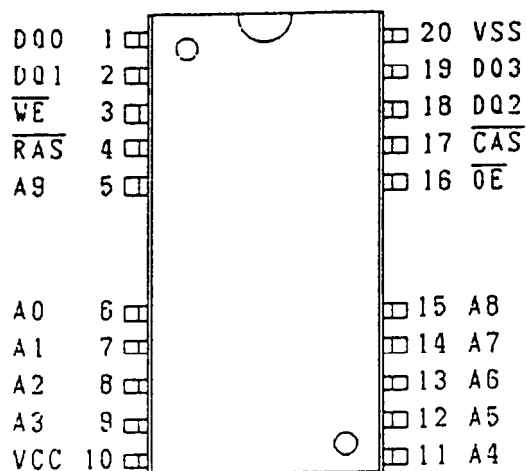
PIN No.	PIN NAME	I/O	DESCRIPTION
1	EFMMON	0	EFM monitor
2	AVCC	-	Power supply for analogue use
3	EFMI	1	EFM signal from RF amplifier
4	AGND	-	GND (analogue)
5	AIN	1	Focus error signal A
6	EIN	1	Tracking error signal E
7	TCG	1	Tracking loss signal
8	BIN	1	Focus error signal B
9	FIN	1	Tracking error signal F
10*	VBAT	1	Detect signal for power supply(servo regulator)
11	WBI	1	ADIP wobble signal
12	VDDI	-	Power supply for digital use
13	DGND	-	Ground for digital use
14/15	TEST0,TEST1	1	Test port(normally,connect to GND)
16	TEST2	1	Test port(normally,connect to GND)
17	X176KO	0	Clock output(f=176.4kHz)
18	FODRF	0	Focus servo forward output. PWM
19	FODRR	0	Focus servo reverse output. PWM
20	TRDRF	0	Tracking servo forward output.PWM
21	TRDRR	0	Tracking servo reverse output.PWM
22	SLDRF	0	Slide servo forward output.PWM
23	SLDRR	0	Slide servo reverse output.PWM
24	SPDRF	0	Spindle servo reverse output or switching motor's rotation
25	SPDRR	0	Spindle servo forward output or switching motor's rotation
26	RAA3	0	Address to D-RAM(ADR3)
27	RAA2	0	Address to D-RAM(ADR2)
28	RAA1	0	Address to D-RAM(ADR1)
29	RAA0	0	Address to D-RAM(ADR0) LSB
30*	RAA10	0	Address to D-RAM(ADR10) MSB
31	VDD2	-	Power supply for interface (D-RAM)
32	RAA4	0	Address output to external D-RAM ADR4
33	RAA5	0	Address output to external D-RAM ADR5
34	RAA6	0	Address output to external D-RAM ADR6
35	RAA7	0	Address output to external D-RAM ADR7
36	RAA8	0	Address output to external D-RAM ADR8
37	RAOEX	0	Data output signal(enable) to external D-RAM
38	DGND	-	Digital section GND
39	RACASX	0	Column address signal output to external D-RAM
40	RAD2	I/O	Data I/O port of external D-RAM D2
41	RAD3	I/O	Data I/O port of external D-RAM D3(MSB)
42	RAD9	0	Data I/O port to D-RAM ADR9
43	RARASX	0	Low address strobe signal to external D-RAM
44	RAWEX	0	Data write enable signal to external D-RAM
45	RAD1	I/O	Data I/o port with external D-RAM D1
46	RAD0	I/O	Data I/O port with external D-RAM D1
47*	TCSR	0	Truck cross signal
48*	ACRCER	0	CRC error Flag monitor output for ADIP
49*	PLCK	0	EFM PLL clock signal output at playback
50	EFM0	0	EFM signal output at recording,CIF monitor output at playback

note : * marks is open port.

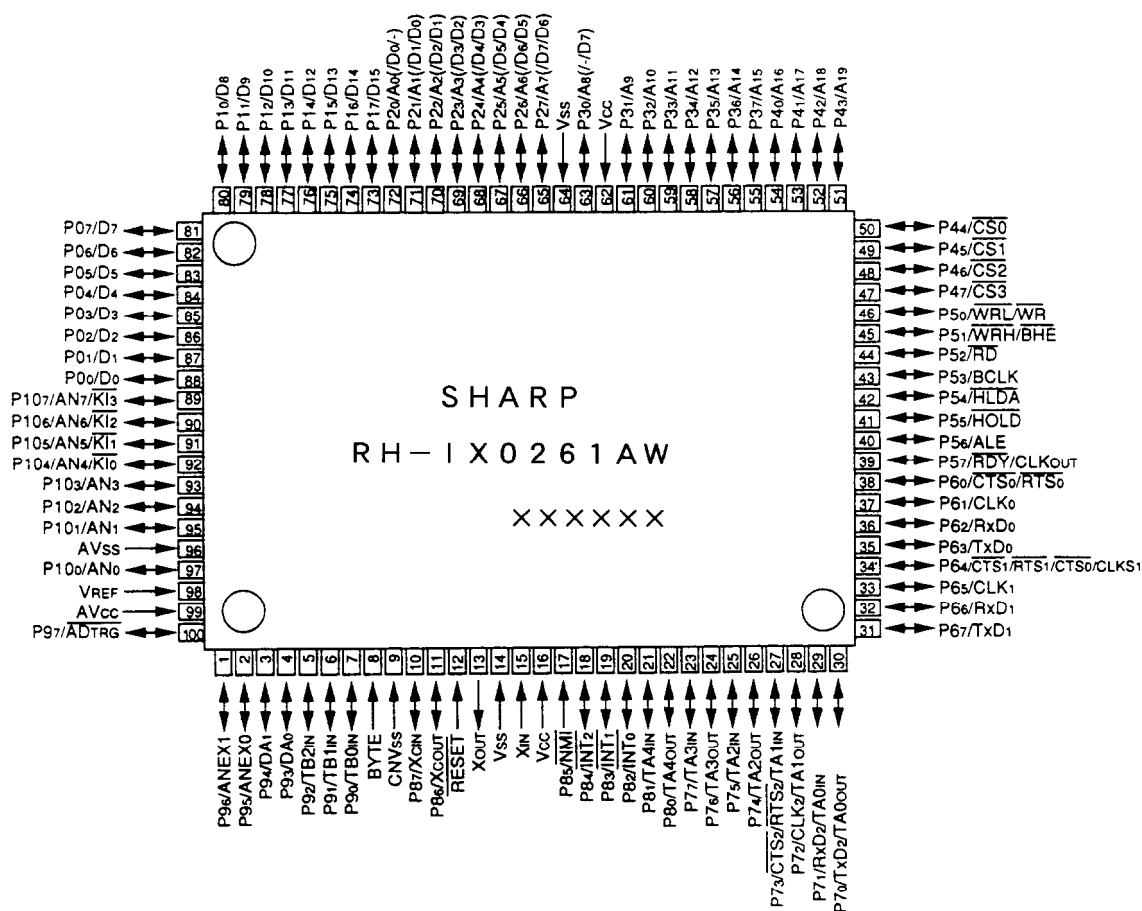
PIN No.	PIN NAME	I/O	DESCRIPTION
51*	X700KO	0	Clock output.f=705.6kHz
52*	EXPORT0	0	Micon.expander output "0"port
53*	EXPORT1	0	Micon.expander output "1"port
54	TESO1	0	When change the PLLR, MICON.expander output port 2
55	TEST3	1/0	When change the PLLOSC, MICON.expander output port 3
56	TEST4	1/0	When change the EXTCLK, MICON.expander output port 4
57	CDDATA	1/0	CD data input port for fast dubbing.
58	CDLRCK	1/0	CDLR clock input for fast dubbing.
59	CDBCLK	1/0	CD bit clock input for fast dubbing.
60	VXI	1	PLL clock input for variable-pitch.
61*	VPO	0	PLL phase deference output for variable-pitch.
62	VDD1	-	Power supply for digital
63	DGND	-	Ground(digital)
64	XI	1	Oscillation circuit (input) 33.8688MHz
65	X0	0	Oscillation circuit (output) 33.8688MHz
66	DIN	1	Digital input signal
67	DOUT	0	Digital output signal
68	VDD3	-	Power supply for internal PLL
69	DGND	-	Ground(digital)
70	LRCK	0	Lch/Rch select output port for music data
71	BCLK	0	Shift clock for music data
72	DFCK	0	Clock for AD/DA converter(digital filter) 256Fs
73	ADDATA	1	Voice input data
74	DADATA	0	Voice output data
75*	FEMON	0	Monitor output for focus error signal
76*	TOTMON	0	Monitor output port for total signal
77*	TEMON	0	Monitor output port for tracking error signal
78*	SBCK	1	Reading clock sub-code
79*	SBO	0	Serial sub DIN code
80*	SBSY	0	Synchronized block sub-code signal
81*	SFSY	0	Synchronized frame sub-code signal
82	FOK	0	Focus detect signal. Focus good is "0" code
83	SENSE	0	Servo condition detect signal
84	COUT	0	Truck cross output signal
85	MCCK	0	Clock output signal for micon.
86	DINTX	0	Interruption output port to system interface
87	VDD1	-	Digital power supply
88	DGND	-	Ground(digital)
89	RSTX	1	Chip reset input(L = reset)
90	SYD0	1/0	Data buss port of interface (LSB)
91 to 96	SYD1 to SYD6	1/0	Data buss port of interface
97	SYD7	1/0	Data buss port of interface (MSB)
98	SYWRX	1	Pulse input port of writing resister
99	SYRDX	1	Pulse input port of reading resister
100	SYRS	1	Resister input select port of interface

note : * marks is open port.

IC12202:IX2474AF (D-RAM)



IC1401:RH-IX0261AW



IC1401:RH-IX0261AW (MECHANISM CONTROL MICROPROCESSOR)

PIN No.	PIN NAME	I/O	DESCRIPTION
1*	P96/ANEX1	O	I/O port P96
2*	P95/ANEX0	O	I/O port P95
3	P94/DA1	O	Adjustment output of laser power
4*	P93/DA0	O	ADJS for auto adjusting step check
5*	P92/TB2IN	O	I/O port P92
6	P91/TB1IN	I	LD SW CK input
7	P90/TB0IN	I	$\overline{\text{ERR}}$ input
8	BYTE	I	GND
9	CNVss	I	GND
10*	P87/XCIN	O	ST-ID output
11*	P86/Xout	O	MD search output
12	$\overline{\text{RESET}}$	I	Reset input
13*	Xout	-	Clock output
14	Vss	-	GND
15	XIN	I	Crystal (8.4672MHz)
16	Vcc	-	+3.15V
17	P85/ $\overline{\text{NMI}}$	I	I/O port P85
18	P84/ $\overline{\text{INT2}}$	I	D INT
19	P83/ $\overline{\text{INT1}}$	I	D SENSE(Servo-sens. input from MD-LSI)
20	P82/ $\overline{\text{INT0}}$	I	ST-ID (MD on)
21	P81/TA4IN	I	CD search input
22	P80/TA4OUT	O	MD RSW input
23*	P77/TA3IN	I	Frequency select for switching power supply
24	P76/TA3OUT	O	Variable pitch output port
25	P75/TA2IN	I	Power failure detect port P-DOWN
26	P74/TA2OUT	O	On/off output for magnetic-head current HDON
27	P73/TA1IN	O	Loading motor output and control signal output LD+
28	P72/TA1OUT	O	Loading motor output and control signal output LD-
29	P71/TA0IN	I	Truck counts signal input CIN
30	P70/TA0OUT	I	Inner switch detect input INN SW
31	P67/TXD1	O	R-DATA
32	P66/RXD1	O	R-LATCH
33	P65/CLK1	O	R-CLK
34	P64/ $\overline{\text{CTS1}}/\overline{\text{RTS1}}/\overline{\text{CTS0}}/\overline{\text{CLKS1}}$	O	Communication's possible to system
35	P63/TXD0	O	MD data output MD DATA
36	P62/RXD0	I	System control data output K DATA
37	P61/CLK0	I	System communication clock input
38	P60/ $\overline{\text{CTS0}}/\overline{\text{CLK}}_{\text{OUT}}$	I	D RAM(4M/16M) input select
39	P57/RDY/CLKOUT	O	REC/PLAY select output port $\overline{\text{R/P}}$
40	P56/ALE		Monitor input for focus servo condition FOK
41*	P55/ $\overline{\text{HOLD}}$	O	I/O port P55
42	P54/ $\overline{\text{HLDA}}$	O	S2 output
43	P53/ $\overline{\text{BCLK}}$	O	S1 output
44	P52/ $\overline{\text{RD}}$	O	Resister select signal (MD-LSI) output SYRS
45	P51/ $\overline{\text{WRH}}/\overline{\text{BHE}}$	O	Lead signal output (MD-LSI) SYRD
46	P50/ $\overline{\text{WRL}}/\overline{\text{WR}}$	O	Write signal output (MD-LSI) SYWR
47	P47/ $\overline{\text{CS3}}$	I/O	Data bus 7 SYS D7
48	P46/ $\overline{\text{CS2}}$	I/O	Data bus 6 SYS D6
49	P45/ $\overline{\text{CS1}}$	I/O	Data bus 5 SYS D5
50	P44/ $\overline{\text{CS0}}$	I/O	Data bus 4 SYS D4

note : * mark is open port

PIN No.	PIN NAME	I/O	DESCRIPTION
51	P43/A19	I/O	Data bus 3 SYS D3
52	P42/A18	I/O	Data bus 2 SYS D2
53	P41/A17	I/O	Data bus 1 SYS D1
54	P40/A16	I/O	Data bus 0 SYS D0
55*	P37/A15	O	I/O port P37
56*	P36/A14	O	I/O port P36
57*	P35/A13	O	I/O port P35
58	P34/A12	O	EEPROM protect mode cancel EEPRO
59	P33/A11	O	EEPROM chip selector output EEPD
60	P32/A10	I/O	EEPROM serial data output
61	P31/A9	O	EEPROM serial clock output
62	Vcc	I	+3.15V
63*	P30/A8	O	I/O port P30
64	Vss	-	Ground
65	P27/A7	O	Soft serial communication ,LSB fast,and 2-mode output L3 DATA
66	P26/A6	O	Soft serial communication ,LSB fast,and 2-mode output L3 MODE
67	P25/A5	O	Soft serial communication ,LSB fast,and 2-mode output L3 CLK
68*	P24/A4	O	I/O port P24
69*	P23/A3	O	I/O port P23
70	P22/A2	O	PCNT0 output
71*	P21/A1	O	I/O port P21
72	P20/A0	O	LDON output
73	P17/D15	O	ANLPTR output
74*	P16/D14	O	ADPON output (for CK)
75*	P15/D13	O	DAPON output (for CK)
76*	P14/D12	O	
77*	P13/D11	O	DSS1 output
78*	P12/D10	O	DIG EX output (for CK)
79*	P11/D9	O	DIG CD output (for CK)
80	P10/D8	O	System reset output XRST
81*	P07/D7	O	ADMUTE output
82*	P06/D6	O	EMPHA output
83*	P05/D5	O	DAMUTE output
84*	P04/D4	O	Mute output
85*	P03/D3	O	DOUTM output
86	P02/D2	I	TEST2 (select special mode12)
87	P01/D1	I	TEST1 (select special mode11)
88	P00/D0	I	TEST0 (select special mode10)
89	P107/AN7/ $\overline{K13}$	I	AVCK3 (special monitor mode input)
90	P106/AN/ $\overline{K12}$	I	Monitor(3.1V) input for AD/DA sections AVCK2
91	P105/AN5/ $\overline{K11}$	I	Monitor(5V) input for DOUT section AVCK1
92	P104/AN4/ $\overline{K10}$	I	Detect input port for temperature DTEMP
93	P103/AN3	I	Rec-input detect MINF
94	P102/AN2	I	Test key input1 TEST K1
95	P101/AN1	I	Test key input2 TEST K2
96	AVss	-	Ground
97	P100/AN0	I	Mecha. and head position detect port HINF
98	VREF	-	+3.15V
99	AVcc	-	+3.15V
100	P97/ $\overline{ADTRG}$	I	I/O port P97

note : * mark is open port

Check before & after replacing the mechanism parts and how to replace it

1. Type Number of Mechanism: MDM-97B

2. Optical Pickup replacement

2-1. Conditions to replace optical pickup

Before replacing the optical pickup, be sure to confirm below listed points and perform the replacement only when abnormal symptom is found. Ask detail analysis with the Disc attached if the cause is uncertain for the problem such as skipping sound. In that case, carefully examine if the Disc is not wrong.

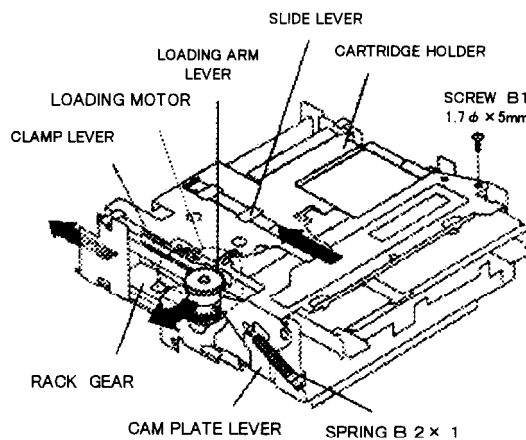
- 1). If error signal at focus or tracking is found abnormal.
- 2). If focus and tracking actuator does not work. (Tracking actuator can be observed by Circuit Tester at #8 and #9 pin of IC M56758FP.)
- 3). Motion of actuator is not smooth enough. (Focus actuator can be confirmed during Search operation by using transparent Disc. Tracking can not be checked with this method.)
- 4). Use Pre-mastered Disc and check if jitters is more than 9.5nsec with KIKUSUI 6135 or if jitters is more than 24nsec with LEADER LJM-1851.
- 5). Use Recordable Disc and check if jitters is more than 11.0nsec with KIKUSUI 6135 or if jitters is more than 28nsec with LEADER LJM-1851.
* If jitters can not be measured at MDM-97B test point, observe the RF wave form by using Oscilloscope with the 10:1 probe.
- 6). If C1 error rate is more than 40 by Pre-mastered Disc.
- 7). If C1 error rate is more than 200 by Recordable Disc.
- 8). If value of Laser Power meter is too low at play mode and recording mode.
- 9). Grating is out of spec, even if the position of spindle motor is aligned.

2-2. How to check

- 1). Check by using separate document "MD-185X MD mechanism measurement methods" for above item 4). to 9). of 2-1.

2-3. Replacement process of optical pickup

- 1). Disconnect 4 sockets (white x2, red and blue colored) and CN1601 (FFC 5 leads) on the PCB.
- 2). Unscrew the 4 screws which fix the PCB. Raise the PCB from the right side of front view, leaving the left side (pick up FFC side) in place. Unlock the CN1101 socket and pull out the FFC.
- 3). Open the roller-arm lever toward the arrow shown in illustration and push back the clamber lever.
- 4). Add 5 volts to the blue color socket of loading motor, push the rack gear toward the arrow and move the cam plate lever until it clatters. (Refer to Fig. 2-1)
- 5). Add 2 volts (Circuit Tester can be used) to the red color socket of pick motor, move the pick to the outermost position and remove the magnetic head. (M1.7 x5 screw)
- 6). Unscrew the screw (M1.7x5) that fix the spring on arm holder and remove the spring at the left side.
- 7). Move the holder arm to the left side and remove it together with cartridge holder.
- 8). Unscrew the 3 screws (M1.7x10) on the molded parts that fix the shaft of the pick up and remove the pick.
If possible, do not remove the mechanism switch PCB by unscrewing the screws (front M1.7x10, back M1.7x3), since it will result difficult work to build the PCB with all of the 3 switches at ON position.
- 9). Install a new pick and confirm the motion. Then, remove the metal fitting from the back side of defective pick and attach it on the new pick by using M1.4x2 screw.
- 10). Attach the holder arm and cartridge holder which were removed at above item 7 and also attach the screws and spring which were removed at above item 6.
- 11). Insert and lock the pick up FFC to the socket CN1101, and fix the PCB by using screws.
- 12). Insert the 2 sockets of the motor (except the blue color) and insert the FFC at CN1601.
- 13). Attach the magnetic head and insert the socket to the PCB.
- 14). Add 5 volts at the blue color socket to move back the mechanism to the EJECT position.



(Fig 2-1)

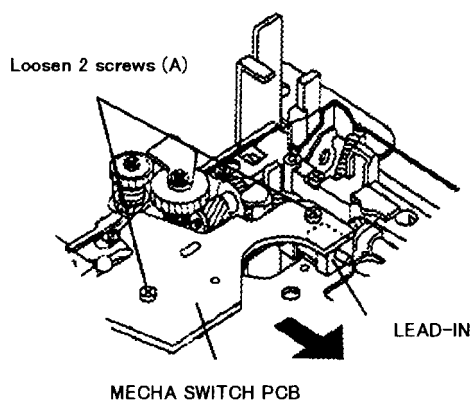
2-4. Check after the replacement

After the replacement, confirm that the following points are within the specifications.

- 1) 1). Write of EEPROM
 - a). Short between pin 2 and pin 8 of IC402 on the MD mechanism PC board. Attach the mechanism on the unit.
 - b). Set the unit to "TEST MODE". (Refer to "How to set test mode " on page 26. (EJECT is displayed)
 - c). Insert the recordable reference disc (TEAC MMD-317 corresponding).
 - d). As AUTO YOBI is displayed, push PLAY/PAUSE key to enter the automatic mode.
 - e). After COMPLETE is displayed, push STOP key. (The indicator is not constant.)
 - f). After wait for approx. 3 seconds, turn the STANDBY key to OFF.
 - g). After wait for approx. 3 seconds, push the ENTER key of the remote control.
 - h). Press EJECT key to take off the disc.

When the disc is not ejected, perform from step b).

 - i). Disconnect the shorted circuit between pins 2 and 8 of IC402 on the mechanism PC board and attach the mechanism assembly on the unit.
- 2). Error rate measured by Pre-mastered Disc must be within the specification.
- 3). Error rate measured by Recordable Disc must be within the specification.
- 4). Error rate at self recording/playback must be within the specification.
- 5). Measurements for the location of lead-in switch must be within the specification. If not, adjust it referring to Fig. 2-2.
- 6). Confirm the Grating is within the specification.



(Fig 2-2)

3. MD Mecha PCB replacement

3-1. Check after the replacement

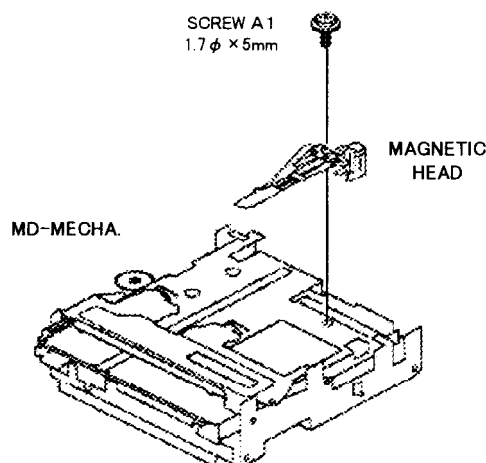
Compatibility with pickup is changed after replacement of MD mechanism PCB.

Please rewrite EEPROM, refer to the item 2-4(1).

4. Magnetic head replacement

4-1. Replacement process

- 1). Pull out the red color socket(CN1604) of slide motor and head(CN1252), add 5 volts at the socket(CN1604) to move the pick up to the outermost position.
- 2). Pull out the socket of magnetic head, unscrew and replace the head. (refer to Fig. 2-3)
- 3). Insert the sockets of magnetic head and slide motor.



(Fig 2-3)

4-2. Check after the replacement

Make recording and measure the error rate by using the recorded disc.
Error rate must be within the specification.

(Note). Refer to separate document (MD-185X MD mechanism measurements methods) for the measurement methods and the specifications.

5. Replace the spindle motor

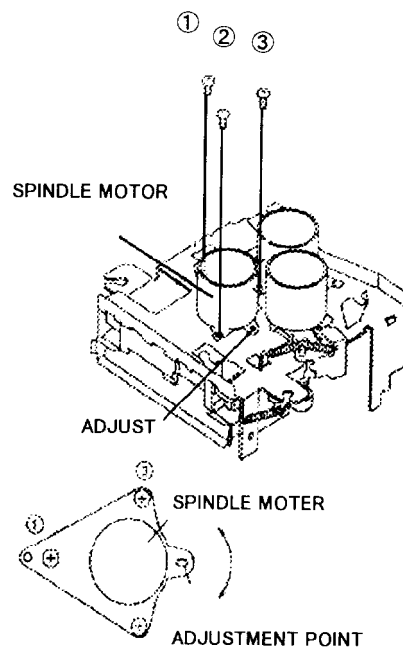
When a motor get old, metallic dust will adhere around the brush and it will decrease the impedance by causing a shor circuit on the coil. This will result an insufficient capability of the driving circuitry and unstable rotating. To detect this, connect a digital multi meter (Circuit Tester is not good) to the moter and rotate the moter may cause skipping etc. slowly by hand. If the resistance decreases at paticular point, the motor is defective. Defective motormay cause skipping sound, etc..

5-1. Procedure to replace the spindle motor

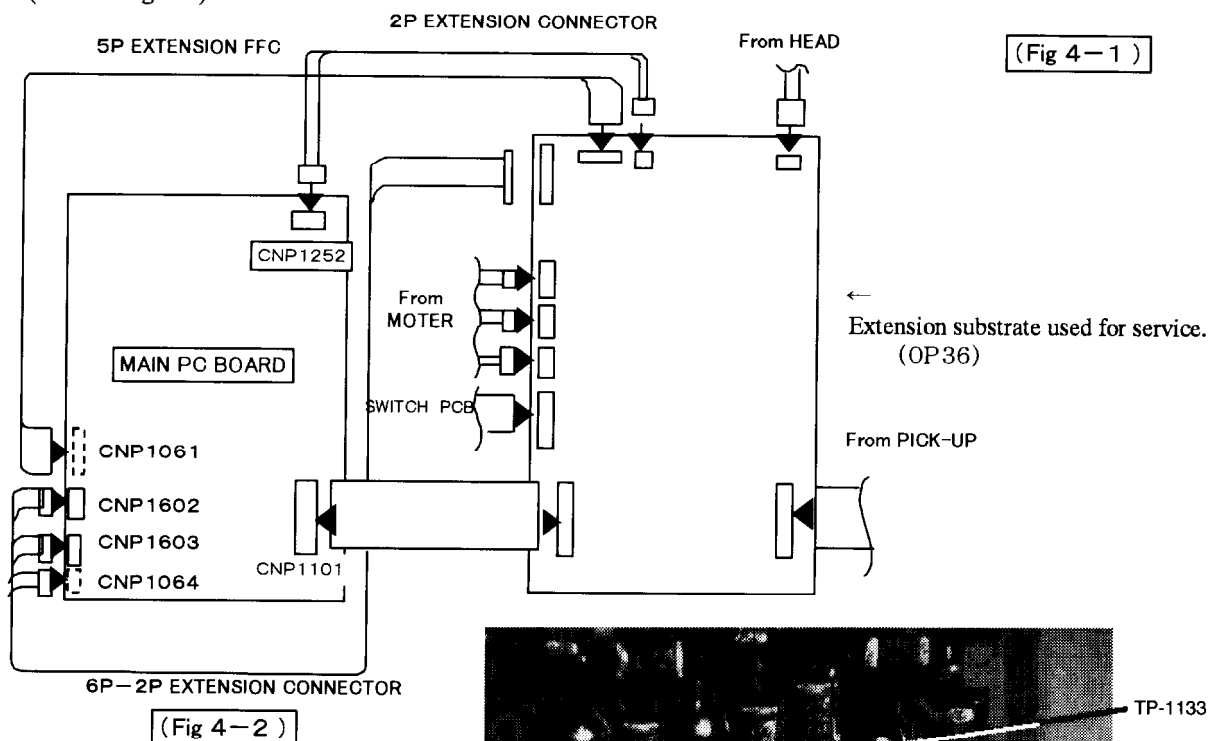
- 1). Disconnect four sockets (white x2, red and blue) and CN1601 (FFC 5 leads) on the PCB.
- 2). Unscrew the four screws which fix the PCB. Raise the PCB from the right side of front view, leaving the left side (pick up FFC side) in place. Unlock the CN1101 socket and pull out the FFC.
- 3). Unscrew the three screws that fix the spindle motor and replace the motor. Fasten the screw temporarily for alignment.

5-2. Alignment after the replacement

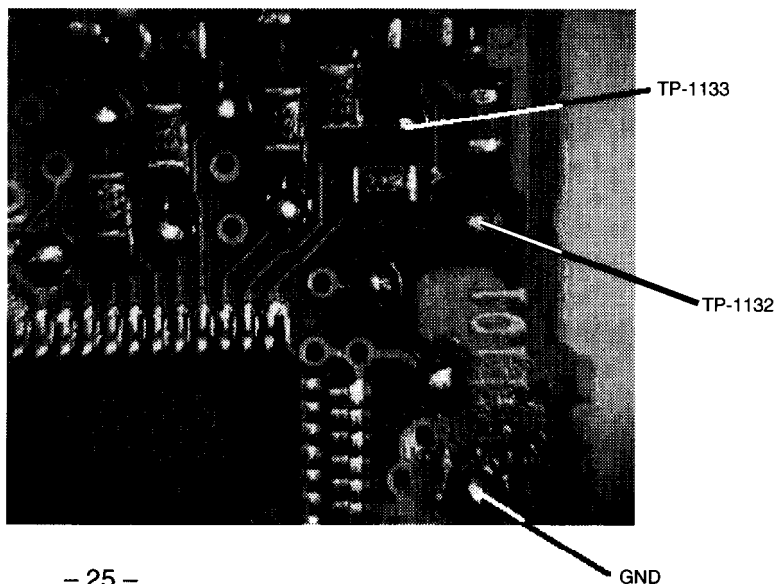
- 1). Use an extension JIG and connect it as shown in Fig. 4-2.
- 2). Connect an oscilloscope to the test points of the PCB as shown in Fig. 4-3. (Refer to Fig. 4-4 for the wirings.) (on page 28)
- 3). Adjust the position of spindle motor while observing the lissajous' figure, by using a screw driverput into the hole for adjustment. (Refer to Fig. 4-4)



(Fig 4-1)



(Fig 4-2)



MD mechanism measurement items and measurement methods

1. Type number of Mechanism: MDM-97B

CAUTION: When replace the optical pick-up, you are not necessary to adjust the "AUTO YOBI".

CAUTION: The automatic adjustment of test mode "AUTO YOBI" is used the reference disc and is performed by the mechanism maker. Do not perform this adjustment.

After "AUTO AJST" is performed every measurement disc, measure the all items.

How to set test mode:

During push the YES key, then push the DISPLAY key and plug in AC plug to the wall outlet.

Or, connect the upper side of R710 (5.6k ohms, at Q701 side) and the chassis. Plug in the AC plug to the wall outlet.

Push the ENTER key of remote control (RC-301MD or RC-339MD) to enter the TEST mode.

2. Error rate

2-1. Pre-mastered Disc

1). During push the YES key, then push the DISPLAY key and plug in AC plug to the wall outlet.

Or, connect the upper side of R710 (5.6k ohms, at Q701 side) and the chassis. Plug in the AC plug to the wall outlet.

2). Push the ENTER key of remote control (RC-301MD or RC-339MD) to enter the TEST mode.

Confirm the display shows "tsm****c***" and then it changes to "EJECT".

3). Insert the Pre-mastered Disc (TMD-381) and confirm the display shows "AUT YOBI".

4). Turn the AMCS knob counterclockwise by one click and confirm the display shows "TEST PLAY".

5). Push INPUT key and confirm the display shows "ADRES 0050".

ADRES shows the location of the pick and lager number means outer location of the disc.

Turning the AMCS knob counterclockwise will change the ADRES number to measure the error rate of desired point.

0050 is the inner, 03C0 is the middle, 0700 is the outer and 08A0 is the outermost position.

6). Push PLAY/PAUSE key. The display shows "s****c****" and error rate measurement will start.

Four digit numerals following the "s" show the subcode address and four digit numerals following the "c" show C1 error rate.

Ascertain the quality of MD mechanism and Disc by the value of error rate. Error rate is defined to be less than 220 in RAINBOW BOOK, although there is no error rate specified for the Pre-mastered Disc. Onkyo has an internal specification for the Pre-mastered Disc error rate, as a criterion to ascertain the MD mechanism.

Pre-mastered Disc error rate $\leq$ 40

Just after starting playback, error rate is displayed as if it is big. However, this is no problem.

2-2. Low reflectivity disc(Recordable Disc and self recording/playback) SONY MDW-74B.

****[TEST REC]->[TEST PLAY]**

1). Push the EJECT key and exchange the disc to Recordable Disc.

2). Confirm the display indicates "AUT YOBI". Turn the AMCS knob counterclockwise and confirm the display shows "TEST REC".

3). Push INPUT key and confirm the display shows "a0050 pw0D". 0050 is address.

If record at inner area, Push the PLAY key.

*How to change address, refer 2-1.5.

*Do not record more than five minutes without interval. It gives serious damage to laser.

4). Push STOP key and confirm the display indicates "TEST REC". Turn the AMCS knob counterclockwise and confirm the display shows "TEST PLAY".

5). Push INPUT key and confirm the display shows "ADRES 0050".

The AMCS knob is turned, and the address recorded in clause 2). is set.

6). Push PLAY/PAUSE key and confirm the display shows "a****c****". Error rate measurement will start.

Four digit numerals following the "a" show the address and four digit numerals following the "c" show C1 error rate.

Similary, Onkyo has an internal specification as shown below.

Self recording/playback disc error rate $\leq$ 200

* In case of self recording/playback disc, rewriting might have been done many times and the song with larger track number is not necessarily located at outer side. Be careful for this when measure the error rate by designating the address.

3. Jitter

3-1. Pre-mastered Disc

- 1). Solder a lead wire at the RF-TP point shown in the Fig. 3-1 PCB picture and connect it to Jittermeter.
- 2). Measure the jitter while playing at the same test modes as error rate measurements.

3). Onkyo internal specifications

KIKUSUI 6135: Less than 9.5nsec

LEADER LJM-1851: Less than 24.0nsec

* Jitter might not be able to measure for MDM-97B mechanism when jitter meter is connected at the test point, due to the unstable RF signal.
In that case, observe the wave form by oscilloscope to ascertain the quality.

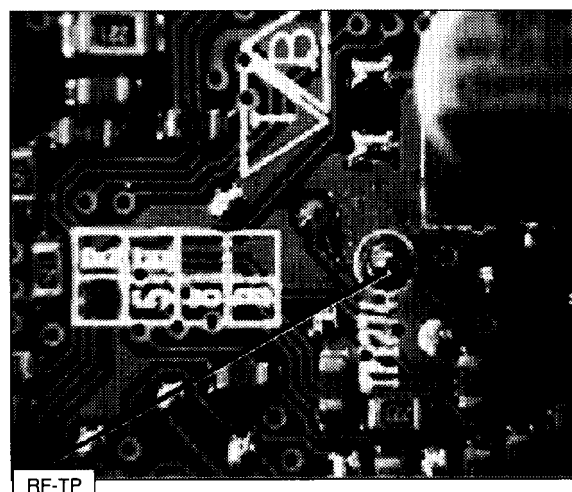


Fig 3-1

3-2. Recordable Disc and Self recording/playback disc

Use the SONY MDW-74B.

- 1). Use same procedure as Pre-mastered Disc for measurements.
- 2). Onkyo internal specifications

KIKUSUI 6135: Less than 11.0nsec

LEADER LJM-1851: Less than 28.0nsec

(Note): Refer Fig. 6-1 for GND connection.

4. Measurement for Read-in position

- 1). During push the YES key, then push the DISPLAY key and plug in AC plug to the wall outlet.
Or, connect the upper side of R710 (5.6k ohms, at Q701 side) and the chassis. Plug in the AC plug to the wall outlet.
- 2). Push the ENTER key of remote control (RC-301MD or RC-339MD) to enter the TEST mode.
Confirm the display shows "tsm***e***" and then it changes to "EJECT".
- 3). Inset the disc and confirm the display shows "AUT YOBI". Push the STOP key and the display will change to "tsm***e***".
- 4). Push DISPLAY key and confirm the display shows "INNER".
- 5). Push PLAY/PAUSE key and confirm the display shows "s****c****".
Four digit numerals following the "s" show the position of read-in switch.
The specification is in between FF85 and FFD2.

5. Laser power measurement

- 1). During push the YES key, then push the DISPLAY key and plug in AC plug to the wall outlet.
Or, connect the upper side of R710 (5.6k ohms, at Q701 side) and the chassis. Plug in the AC plug to the wall outlet.
- 2). Push the ENTER key of remote control (RC-301MD or RC-339MD) to enter the TEST mode.
Confirm the display shows "tsm***e***" and then it changes to "EJECT".
- 3). Put the optical sensor of laser power meter (LEADER 8001 or 8010) to the objective lens of pick up.
- 4). Each time to push INPUT key will change the display to "xpw", "rpw" and "ppw". Measure the power for each position.
Do not keep the "rpw" and "xpw" conditions for long. Especially, measure the power for "xpw" within 15 seconds.

Reference value: "xpw" 6.8 mW +/- 5%

"rpw" 5.2 mW +/- 5%

"Ppw" 0.62mW +/- 5%

6. Grating check

- 1). Solder lead wires at the points shown in the Fig. 6-1 PCB picture and connect them to oscilloscope as shown in Fig. 4-4. Set the oscilloscope as shown in Fig. 4-4.
Set the oscilloscope at AC input, 20mV/DIV range.

- 2). During push the YES key, then push the DISPLAY key and plug in AC plug to the wall outlet.
Or, connect the upper side of R710 (5.6k ohms, at Q701 side) and the chassis. Plug in the AC plug to the wall outlet.
- 3). Push the ENTER key of remote control (RC-301MD or RC-339MD) to enter the TEST mode.
Confirm the display shows "tsm****e**" and then it changes to "EJECT".
- 4). Insert the Pre-mastered Disc and confirm the display shows "AUT YOBI". Push STOP key and the display will change to "tsm****e**".
- 5). Turn the AMCS knob clockwise by two clicks and confirm the display shows "AUTO AJST". Push PLAY/PAUSE key.
- 6). Confirm the display changes to "PEG:", "HAG:" and then "COMPLETE". Wave form of the Gratingis shown in the oscilloscope.

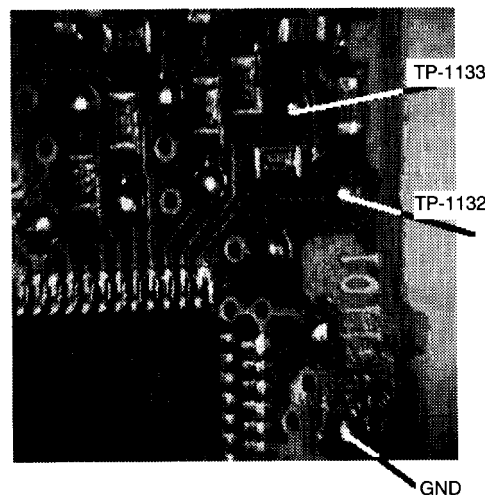
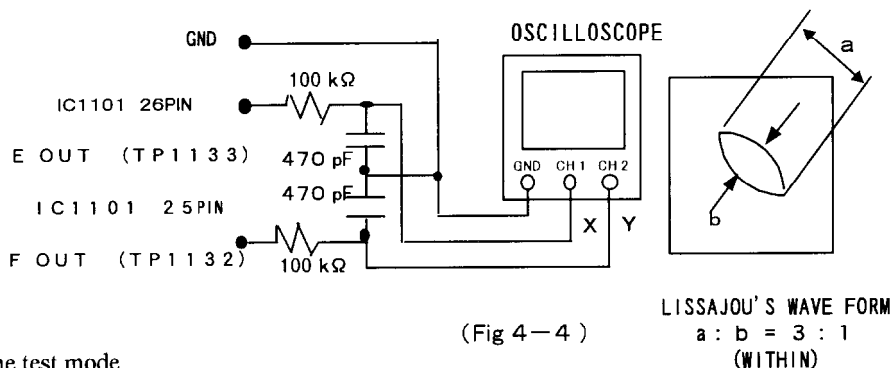


Fig 6-1

- 7). When adjust the grating, refer to the item 5-1 and the figure 4-1 on page 25.
3 screws of spindle motor few loosen, then readjusting screws while look at the oscilloscope.

Pick-up grating measurement



7. Cancel the test mode

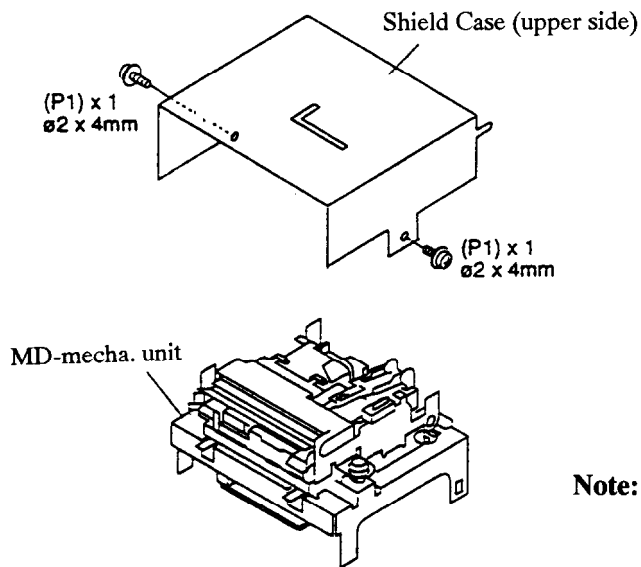
Unplug the AC plug from wall outlet to cancel the test mode.

Message List

The following table explain the various messages that appear in the display.

Message	meaning
Blank Disc	A new recordable MD or a recordable MD without disc or track names is inserted.
Cannot Copy	An attempt was made to make a second copy from a digitally dubbed MD.
Cannot Edit	An attempt was made to edit the disc during MEMORY or RANDOM play, or in recording standby mode, or an attempt to edit a premastered disc was made.
Cannot Rec	An attempt was made to record onto a premastered (read-only) disc.
D. In Unlock	The digital equipment (CD player, DAT, etc) has not been connected properly. Otherwise, the connected digital equipment is not operating properly.
Disc Error	The disc is abnormal (scratched or missing a TOC).
Disc Full	The disc is full.
Impossible	The disc could not be edited.
Mecha Error	An error occurred in the unit's internal mechanism.
Memory Full	An attempt was made to record a 26th track.
Full	The titling capacity of the disc has reached its limit.
Name Full	Number of characters has reached the limit for input to one MD.
No Change	The naming attempt failed.
No Disc	There is no disc in the unit.
No Track	The inserted disc has a disc title but no tracks.
Not Audio	An attempt was made to play a non-audio disc.
Over	In pause mode (when playing is paused): the Fast Forward button (▶▶) was pressed to the end of the disc.
Protected	The inserted disc is record-protected.
Retry Error	The recording attempt failed due to a consecutive disturbance or scratches on the MD.
Temp Over	An abnormally high temperature is detected in the unit's internal mechanism.
TOC Error	The reading of the TOC failed.
TOC Writing	The unit is writing the recorded or edited contents to the MD.
U-TOC Error	The writing of the recorded or edited contents failed.

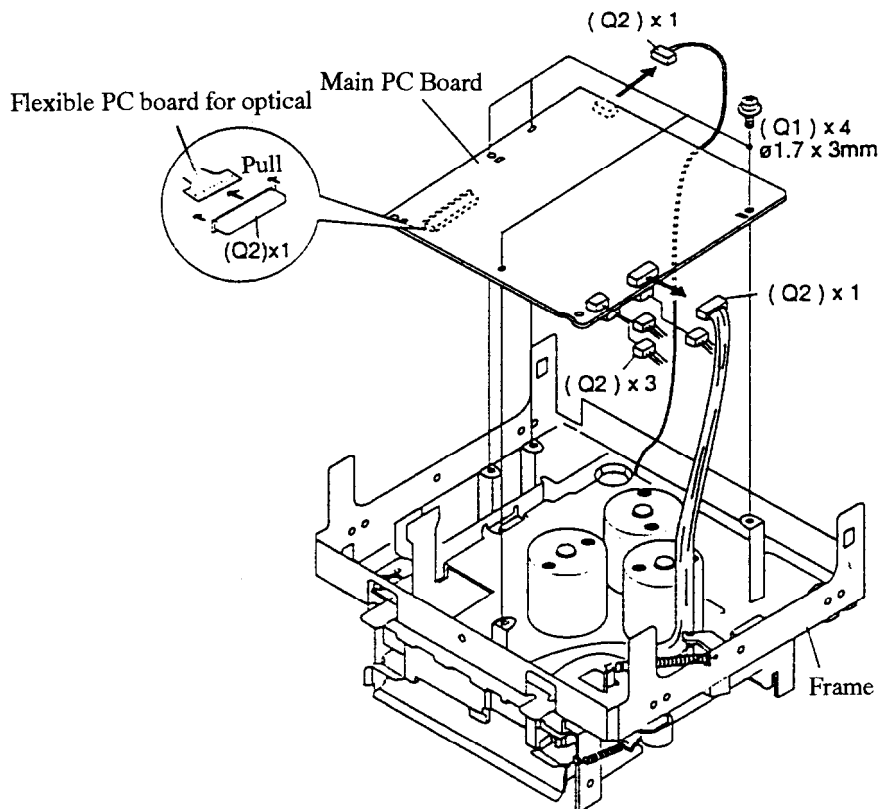
MD MECHANISM DISASSEMBLY



Procedure to reset the Mechanism

Note: Be sure to reset the mechanism, when you exchanged it. Follow next procedure for the reset.

- 1) Remove the top cover of the unit.
- 2) Temporarily connect either of RESET MECHA terminals (2p terminal) on the main PCB (NAAR-6419) to the chassis (GND) by using a lead wire with clips at each side.
- 3) Plug in the AC plug to the wall outlet, keeping above lead wire connected.
- 4) Disconnect above lead wire.
- 5) Turn on the unit.



MD MECHANISM SECTION

- Remove the optical head
(refer to Fig.1)

1.The screw(A1) is taken off.

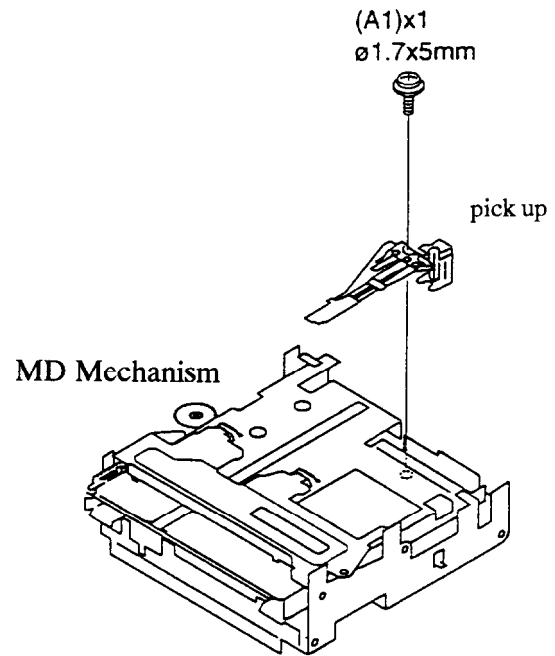


Fig.1

- Cartridge holder is taken off.
(refer to Fig.2)

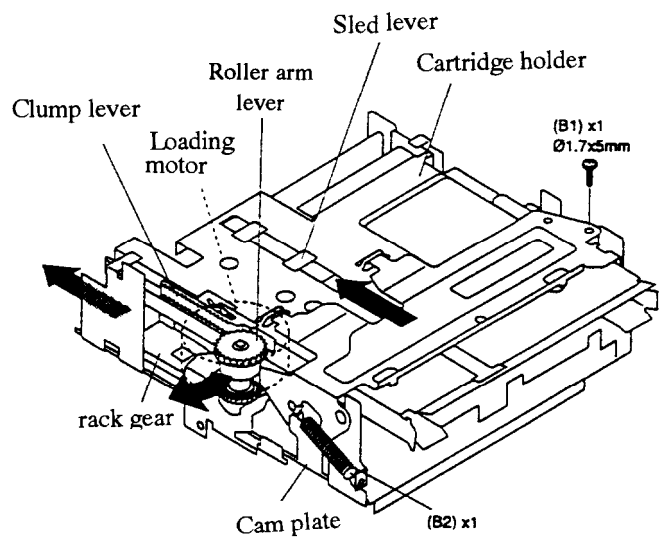


Fig.2

- Mecha switch PC board is taken off.
(refer to Fig.3)

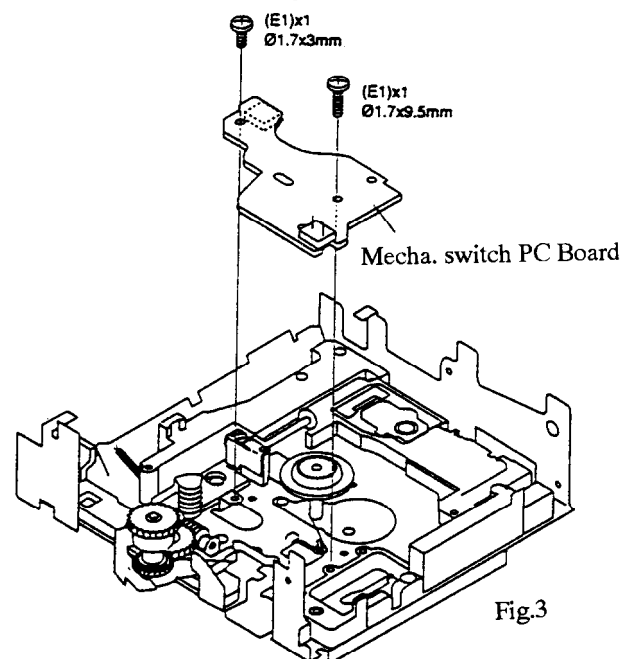


Fig.3

- Sled motor and Loading motor is taken off.
(refer to Fig.4)

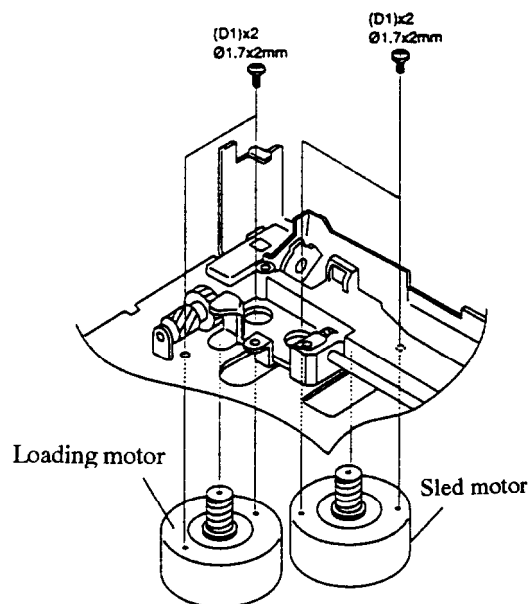


Fig.4

- Spindle motor is taken off.
(refer to Fig.5)

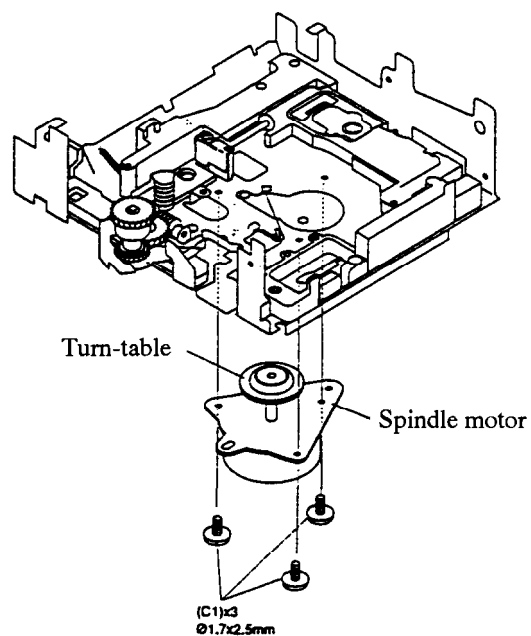


Fig.5

- Optical pick up is taken off.
(refer to Fig.6)

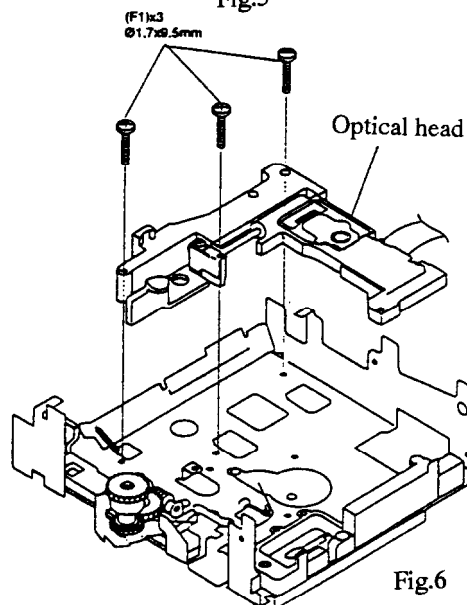


Fig.6

● Mechanism Adjustment

Pick-up grating measurement

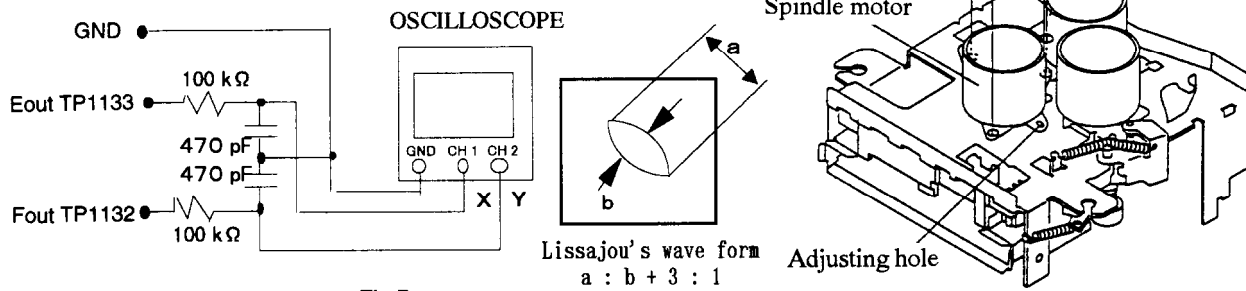


Fig.7

Fig.7

JITTER CHECKING

- 1) Insert the Pre-mastered disc.
- 2) Setting is AUTO Mode (TEST MODE) and then auto adjust. (Indicate on display "COMPLETE")
- 3) 3 screws of spindle motor few loosen, then re-adjusting screws while look at the oscilloscope.

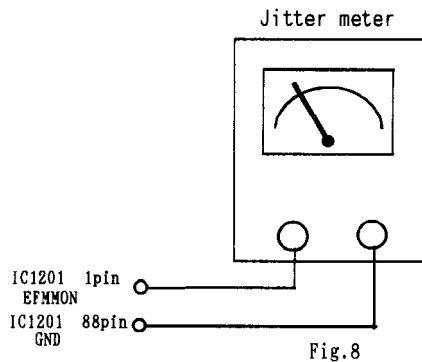


Fig.8

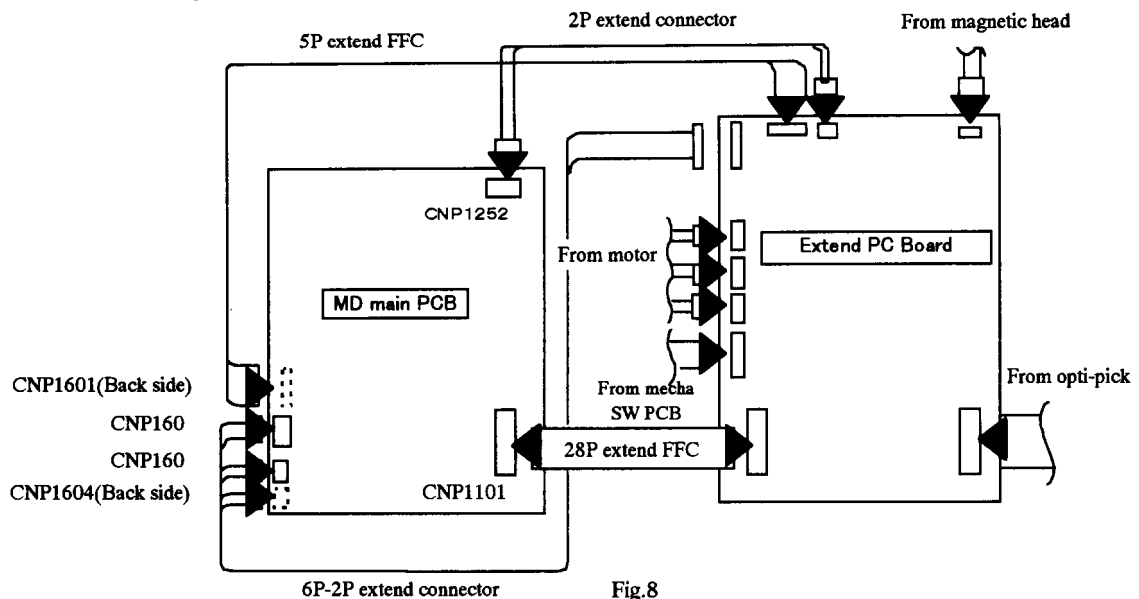
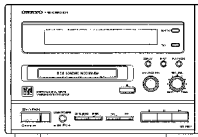


Fig.8

ONKYO® SERVICE MANUAL

MINIDISC RECORDER MODEL MD-185II



Black and Silver models

UDT UPT	230V AC, 50Hz
UDT	120V AC, 60Hz

SAFETY-RELATED COMPONENT WARNING!!

COMPONENTS IDENTIFIED BY MARK Δ ON THE SCHEMATIC DIAGRAM AND IN THE PARTS LIST ARE CRITICAL FOR RISK OF FIRE AND ELECTRIC SHOCK. REPLACE THESE COMPONENTS WITH ONKYO PARTS WHOSE PARTS NUMBERS APPEAR AS SHOWN IN THIS MANUAL.

MAKE LEAKAGE-CURRENT OR RESISTANCE MEASUREMENTS TO DETERMINE THAT EXPOSED PARTS ARE ACCEPTABLY INSULATED FROM THE SUPPLY CIRCUIT BEFORE RETURNING THE APPLIANCE TO THE CUSTOMER.

SPECIFICATIONS

System:	MiniDisc digital audio system
Recording system:	Magnetic field modulation overwrite system
Signal read out system:	Optical non-contact
Recording time:	Max. 74 min. (when using MDW-74)
Number of revolutions:	About 400-900 rpm (constant linear velocity)
Error correction system:	Advanced Cross Interleave Reed-Solomon code
Sampling frequency:	44.1 kHz
Number of channels:	2 (stereo)
Frequency response:	15 Hz - 18 kHz (± 1 dB)
Signal to noise ratio:	98 dB or more when playing
Output level:	1.3 volts r.m.s.
Power supply rating:	AC 230 V, 50 Hz AC 120 V, 60 Hz
Power consumption:	14 watts
Dimensions(W×H×D):	185 × 131 × 299mm (7-5/8" × 5-3/16" × 11-3/4")
Weight:	3.1 kg (6.8 lbs.)

Specifications and features are subject to change without notice.

ONKYO®
AUDIO COMPONENTS

SERVICE SAFETY PRECAUTIONS

1. Replacing the fuses

 This symbol located near the fuse indicates that the fuse used is fast operating type. For continued protection against fire hazard, replace with same type fuse. For fuse rating refer to the marking adjacent to the symbol.

 Ce symbole indique que le fusible utilise est a rapide.

Pour une protection permanents, n'utiliser que des fusibles de meme type.

Ce dernier est indique la qu le present symbol est appose.

Circuit No.	Part No.	Description
F901	252086	0.1 6A-SE-EAWK <P,PT>
F901	252192	0.4A-TSC <DT>
F903	252075	2.5A-SE-EAK

NOTE: <P> 230V model only
 <PT> Asian model only
 <DT> Taiwanese model only

TABLE OF CONTENTS

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Caution on replacement of optical pick-up	3
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Packing view / parts list	37

CAUTION ON REPLACEMENT OF OPTIONAL PICKUP

The laser diode in the optical pickup block is sensitive to static electricity, surge current and etc. The components are liable to be damaged or its reliability remarkably deteriorated.

During repair, carefully take the following precautions. (The following precautions are included in the service parts.)

PRECAUTIONS

1. Ground for the work-desk.

Place a conductive sheet such as a sheet of copper (with impedance lower than 10Mohm) on the work-desk and place the set on the conductive sheet so that the chassis can be grounded.

2. Grounding for the test equipments and tools.

Test equipments and toolings should be grounded in order that their ground level is the same the ground of the power source.

3. Grounding for the human body.

Be sure to put on a wrist-strap that is properly grounded.

Be particularly careful when wearing synthetic fiber clothes, or air is dry.

4. Select a soldering iron that permits no leakage and have the tip of the iron well-grounded.

5. Do not check the laser diode terminals with the probe of a circuit tester or oscilloscope.

PROTECTION OF EYES FROM LASER BEAM DURING SERVICING

This set employs a laser. Therefore, be sure to carefully follow the instructions below when servicing.

WARNING !!

SERVICE WARNING: DO NOT APPROACH THE LASER EXIT WITH THE EYES TOO CLOSELY. IN CASE IT IS NECESSARY TO CONFIRM LASER BEAM EMISSION, BE SURE TO OBSERVE FROM A DISTANCE OF MORE THAN 30cm FROM THE SURFACE OF THE OBJECTIVE LENS ON THE OPTICAL PICK-UP BLOCK.

Laser Diode Properties

Material:	GaAlAs
Wavelength:	760-800nm
Emission Duration:	continuous
Laser output:	max. 0.5mW*

* This output is the value measure at a distance about 1.8mm from the objective lens surface on the Optical pick-up Block.

LASER WARNING LABEL

These labels are located on the mechanism.

The label shown below are affixed.

1. Warning label

This label is location on the chassis.

DANGER —INVISIBLE LASER RADIATION
WHEN OPEN AND INTERLOCK FAILED OR
DEFEATED. AVOID DIRECT EXPOSURE TO BEAM.

CAUTION —HAZARDOUS LASER AND
ELECTROMAGNETIC RADIATION WHEN OPEN
AND INTERLOCK DEFEATED

ATTENTION —RAYONNEMENT LASER
ET ELECTROMAGNETIQUE DANGEREUX SI
OUVERT AVEC L'ECLAUCHEMENT DE SECURITE
ANNULE.



VARNING

UDELIG LASERSTRÅLING NÄR DESSA DEL
ÄR ÖPPNA OCH SPÄRRINGEN ÄR UTSKÖVAD.
ÅRSTÄNDIGT FÖR STRÅLING.

VARO!

VAIKUTUSVAIKUTUSVAIKUTUSVAIKUTUS
SUUT AUTOMATISMAATTOMALLI
LASERSTRÄLLE. KÄÄNÄSÄÄRÄNÄSÄ.

ADVARSEL

Denne maskine er anbragt på apparatets højre side og indikerer,
at apparatet arbejder med laserstråler af klasse 1, hvilket betyder,
at der anvendes laserstråler af svageste klasse, og at man ikke
på apparatets yderside kan blive udsat for uønsket kraftig
stråling.

ADVARSEL: UDELIG LASERSTRÅLING
VED ÅBNING, NÄR SPÄRRINGEN
BRYTER ER UDE AT FUNKTION.
UNDGÅ UDSÄTTELSE FÖR STRÅLING.

ADVARSEL
UDELIG LASERSTRÅLING NÄR DESSA
ÄR ÖPPNA OCH SPÄRRINGEN BRYTES
UNDGÅ EXPOSERING FÖR STRÅLEN.

APPARATET BÖR KUN ÅBNES AFFÄGIFOLK MED SERLIGT
KENDSKAB TIL APPARATER MED LASERSTRÅLER!

Indvendigt i apparatet er anbragt den her gengivne
advarselsskiltning, som advarer mod at foretage sådanne indgreb
i apparatet, at man kan komme til at udsætte sig for laserstråling

2. Class 1 label

This label is located on the left side of rear cover.

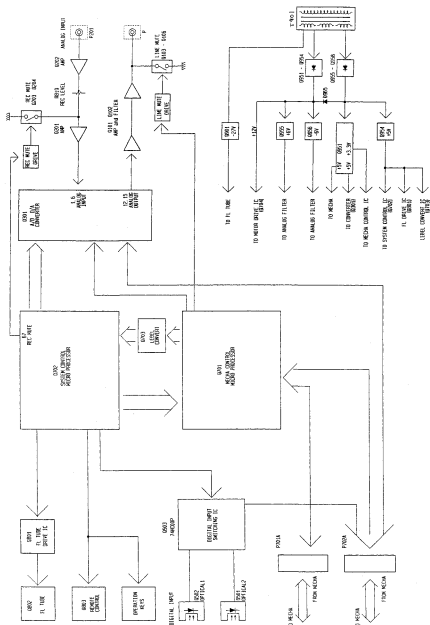
"CLASS 1 LASER
PRODUCT"

LUOKAN 1
LASERLAITE

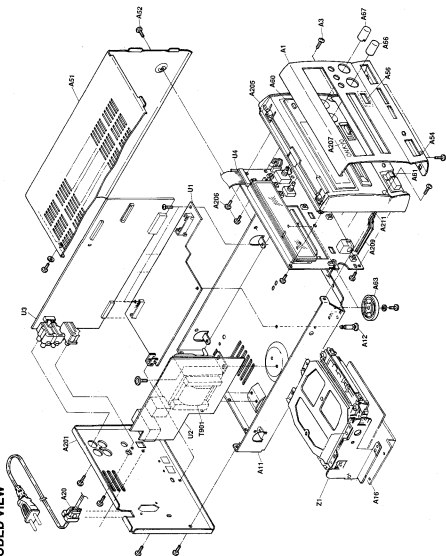
KLASS 1
LASER APPARAT

VAROITUS! LAITTEEN KAYTTAMINEN MUULLA KUN
TASSA KAYTTDOHJEESSA MAINTULLA TAVALLA
SAATTAA ALTISTAA KAYTTAJAN TURVALLISUUSLUOKAN
1 YLITTAVALLE NAKYMATTOMALLE LASERSATEILLYLLE

BLOCK DIAGRAM



EXPLODED VIEW



PARTS LIST

REF. NO.	PART NO.	DESCRIPTION	REF. NO.	PART NO.	DESCRIPTION
A1	27110986A	Front Bracket AS <S>	A211	283255359	Knob (POW) <S>
A1	27110987A	Front Bracket AS 	A211	283255492	Knob (POW)
A3	838130088	3TTTB+8B, Self-tapping screw	A211	271190803	Holder
A11	27100307A	Chassis	A210	830440089	4TTC+8C(BC), Self-tapping screw
A12	27190266	Holder, KGLS-12RT	A216	830440109	4TTC+10C(BC), Self-tapping screw
A16	27190999A	Holder(M)	Z1	1H3481322	MD Mecha AS
A18	27270393	Spacer	E801	2045191512	NCFC5-191512, Flexible flat cable
A20	27300750	Cord Busing, #2271	E802	2045211512	NCFC5-211512, Flexible flat cable
A31	28184626A	Top cover <S>	E803	2045291512	NCFC5-291512, Flexible flat cable
A51	28184625A	Top cover 	E831	253237HIT	Δ AS-CEE, AC Cord <P>PT>
A52	838230088	3TTTB+8B(ND), Self-tapping screw	E831	253192HIT	Δ AS-UC-6W18, AC Cord <DT>
A52	838430088	3TTTB+8B(BC), Self-tapping screw	E832	29362028	Label (ALL) <DT>
A53	28141165	Cushion	E835	838130088	3TTTB+8B, Self-tapping screw <DT>
A54	28198831	Facet	P901	252086	Δ 0.16A-SE-EAWK, Fuse
A55	27190970	Holder (FA) <S>	P901	252192	Δ 0.4A-TSC, Fuse <DT>
A55	27190969	Holder (FA) 	P903	252075	Δ 2.5A-SE-EAK, Fuse
A56	27267892	Guide (OPEN) <S>	T901	2301261	Δ NPT-1296D, Power transformer <DT>
A56	27267891	Guide (OPEN) 	T901	2301262	Δ NPT-1296P, Power transformer <P>PT>
A59	28133340A	Back plate <S>	U1	1H348599-1B	NAAR-5999-1B, Digital circuit pc board <P>PT>
A59	28133344	Back plate 	U2	1H348599-1C	NAAR-5999-1C, Digital circuit pc board <DT>
A60	28191774	Clear plate	U2	1H348502-1B	NAETC-5902-1B, Power transformer pc board <P>PT>
A61	28191712	Clear plate (RE)	U3	1H348502-1C	NAETC-5902-1C, Power transformer pc board <DT>
A63	27175252-1BY	Leg AS	U3	1H348501-1B	NAPS-5901-1B, Power supply circuit pc board <P>PT>
A66	28325230	Knob (AMC)	U4	1H348501-1C	NAPS-5901-1C, Power supply circuit pc board <DT>
A67	28325232	Knob (REC)	U4	1H348500-1B	NADIS-5900-1B, Display circuit pc board <P>PT>
A201	27122320	Rear panel <P>PT>	U4	1H348500-1C	NADIS-5900-1C, Display circuit pc board <DT>
A201	27122319	Rear panel <DT>	U5	1H348503-1B	NAETC-5903-1B, AC Outlet pc board <P>PT>
A203	838430107	3TTTB+10S(BS), Self-tapping screw	U5	1H348503-1C	NAETC-5903-1C, AC Outlet pc board <DT>
A205	27211901	Front panel <S>			
A205	27211902	Front panel 			
A206	801525	3TTTB+8B(BC), Self-tapping screw			
A206	838130088	3TTTB+8B, Self-tapping screw			
A207	28135241Y	Badge			
A209	27273163A	Joint (POW)			

NOTE:

<S> Silver model only

 Black model only

<P> 230V model only

<PT> Asian model only

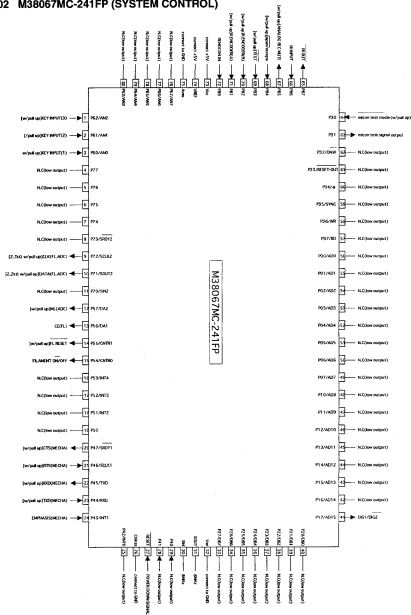
<DT> Taiwan model only

NOTE: THE COMPONENTS IDENTIFIED BY MARK

Δ ARE CRITICAL FOR RISK OF FIRE AND
ELECTRIC SHOCK. REPLACE ONLY WITH
PART NUMBER SPECIFIED.

IC BLOCK DIAGRAM AND DESCRIPTION

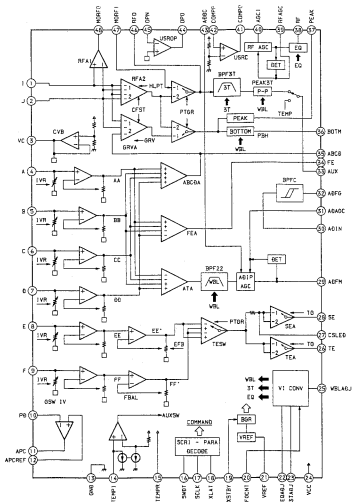
Q702 M38067MC-241FP (SYSTEM CONTROL)



PIN NO.	SYMBOL	DESCRIPTION	SYMBOL	DESCRIPTION
42	N/C	Test switch input port 3 (A/D input)	N/C	not used (Low output)
43	N/C	Test switch input port 2 (A/D input)	N/C	not used (Low output)
44	N/C	Test switch input port 1 (A/D input)	N/C	not used (Low output)
45	N/C	not used (Low output)	N/C	not used (Low output)
46	N/C	not used (Low output)	N/C	not used (Low output)
47	N/C	not used (Low output)	N/C	not used (Low output)
48	N/C	not used (Low output)	N/C	not used (Low output)
49	N/C	not used (Low output)	N/C	not used (Low output)
50	N/C	Serial clock data output terminal (EL DRIVER CODEC)	N/C	not used (Low output)
51	N/C	Serial clock output terminal (EL DRIVER CODEC)	N/C	not used (Low output)
52	N/C	data latch signal output terminal for CODEC	N/C	not used (Low output)
53	N/C	Reset signal output terminal for IFL DRIVER	N/C	not used (Low output)
54	N/C	Data latch signal output terminal for IFL DRIVER	N/C	not used (Low output)
55	N/C	Reset signal output terminal for IFL DRIVER	N/C	not used (Low output)
56	N/C	Fluorescent power supply control output terminal (at low: on)	N/C	not used (Low output)
57	N/C	not used (Low output)	N/C	not used (Low output)
58	N/C	not used (Low output)	N/C	not used (Low output)
59	N/C	not used (Low output)	N/C	not used (Low output)
60	N/C	not used (Low output)	N/C	not used (Low output)
61	N/C	not used (Low output)	N/C	not used (Low output)
62	N/C	not used (Low output)	N/C	not used (Low output)
63	ISSET SIGNAL (N/C)	Test signal output terminal for microprocessor (at low: check mode) (terminal: not used)	ISSET SIGNAL (N/C)	Test signal output terminal for microprocessor (at low: check mode) (terminal: not used)
64	MICOM TEST	Test signal input terminal for microprocessor (at high: on)	MICOM TEST	Test signal input terminal for microprocessor (at high: on)
65	RL OUT	RF card output terminal (Logic output)	RL OUT	RF card output terminal (Logic output)
66	RL IN/IT	Analog test mode control output terminal (at high: on)	RL IN/IT	Analog test mode control output terminal (at high: on)
67	ANALOG REC MUTE	Japan/Europe area select input terminal (at low: Japan)	ANALOG REC MUTE	Japan/Europe area select input terminal (at low: Japan)
68	EXT	Power input terminal for Rotary encoder	EXT	Power input terminal for Rotary encoder
69	MICHA TEST	Rotary encoder for pads input terminal	MICHA TEST	Rotary encoder for pads input terminal
70	R-ENCODER(B)	Remote control signal	R-ENCODER(B)	Remote control signal
71	R-ENCODER(A)	Power supply (connect +5V)	R-ENCODER(A)	Power supply (connect +5V)
72	REMOCOM IN	A/D D/A converter power supply terminal (connect +5V)	REMOCOM IN	A/D D/A converter power supply terminal (connect +5V)
73	VREF	AGND converter power supply terminal	VREF	AGND converter power supply terminal
74	AVREF	not used (Low output)	AVREF	not used (Low output)
75	AVREF	not used (Low output)	AVREF	not used (Low output)
76	N/C	not used (Low output)	N/C	not used (Low output)
77	N/C	not used (Low output)	N/C	not used (Low output)
78	N/C	not used (Low output)	N/C	not used (Low output)
79	N/C	not used (Low output)	N/C	not used (Low output)
80	N/C	not used (Low output)	N/C	not used (Low output)

PIN NO.	SYMBOL	DESCRIPTION	SYMBOL	DESCRIPTION
1	KEY INPUT(3)	Test switch input port 3 (A/D input)	KEY INPUT(3)	Test switch input port 3 (A/D input)
2	KEY INPUT(2)	Test switch input port 2 (A/D input)	KEY INPUT(2)	Test switch input port 2 (A/D input)
3	KEY INPUT(1)	Test switch input port 1 (A/D input)	KEY INPUT(1)	Test switch input port 1 (A/D input)
4	N/C	not used (Low output)	N/C	not used (Low output)
5	N/C	not used (Low output)	N/C	not used (Low output)
6	N/C	not used (Low output)	N/C	not used (Low output)
7	N/C	not used (Low output)	N/C	not used (Low output)
8	N/C	not used (Low output)	N/C	not used (Low output)
9	CLK	Serial clock data output terminal (EL DRIVER CODEC)	CLK	Serial clock data output terminal (EL DRIVER CODEC)
10	DATA	Serial clock output terminal (EL DRIVER CODEC)	DATA	Serial clock output terminal (EL DRIVER CODEC)
11	N/C	not used (Low output)	N/C	not used (Low output)
12	ML	data latch signal output terminal for CODEC	ML	data latch signal output terminal for CODEC
13	CE	Reset signal output terminal for IFL DRIVER	CE	Reset signal output terminal for IFL DRIVER
14	FL RESET	Data latch signal output terminal for IFL DRIVER	FL RESET	Data latch signal output terminal for IFL DRIVER
15	FILAMENT	Fluorescent power supply control output terminal (at low: on)	FILAMENT	Fluorescent power supply control output terminal (at low: on)
16	N/C	not used (Low output)	N/C	not used (Low output)
17	N/C	not used (Low output)	N/C	not used (Low output)
18	N/C	not used (Low output)	N/C	not used (Low output)
19	N/C	not used (Low output)	N/C	not used (Low output)
20	MICHA CTS	Mechanism microprocessor CTS terminal output	MICHA CTS	Mechanism microprocessor CTS terminal output
21	MICHA RTS	Mechanism microprocessor DATA terminal input	MICHA RTS	Mechanism microprocessor DATA terminal input
22	MICHA RXD	Mechanism microprocessor RXD terminal output	MICHA RXD	Mechanism microprocessor RXD terminal output
23	MICHA TXD	Mechanism microprocessor TXD terminal input	MICHA TXD	Mechanism microprocessor TXD terminal input
24	EMPHASIS	Emphasis information input terminal (High/EMPHASIS ON 50% RICE)	EMPHASIS	Emphasis information input terminal (High/EMPHASIS ON 50% RICE)
25	N/C	not used (Low output)	N/C	not used (Low output)
26	CVREF	GND	CVREF	GND
27	RESET	System reset signal input terminal (at low: reset)	RESET	System reset signal input terminal (at low: reset)
28	N/C	not used (Low output)	N/C	not used (Low output)
29	N/C	not used (Low output)	N/C	not used (Low output)
30	XIN	Ceramic oscillator (3MHz)	XIN	Ceramic oscillator (3MHz)
31	XOUT	Crystal oscillator (3MHz)	XOUT	Crystal oscillator (3MHz)
32	Vcc	GND	Vcc	GND
33	N/C	not used (Low output)	N/C	not used (Low output)
34	N/C	not used (Low output)	N/C	not used (Low output)
35	N/C	not used (Low output)	N/C	not used (Low output)
36	N/C	not used (Low output)	N/C	not used (Low output)
37	N/C	not used (Low output)	N/C	not used (Low output)
38	N/C	not used (Low output)	N/C	not used (Low output)
39	N/C	not used (Low output)	N/C	not used (Low output)
40	N/C	not used (Low output)	N/C	not used (Low output)
41	DIG1/DIG2	Digital input selector signal output terminal (at High: digital input 1)	DIG1/DIG2	Digital input selector signal output terminal (at High: digital input 1)

IC101 CXA2523R (RFAMP)



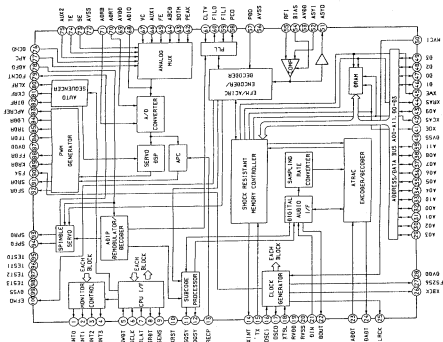
IC101 CXA2523R (RF AMP.)

PIN NO.	SYMBOL	I/O	DESCRIPTION
1	I	I	RF signal I input for I-V converter
2	J	I	RF signal J input for I-V converter
3	VC	O	Mid-point voltage input terminal.
4~9	A~F	I	Input signal for optical pick-up detector
10	PD	I	Optical monitor input
11	APC	O	Laser APC output
12	APCREF	I	Laser power setting standard voltage input
13	GND	-	GND
14	TEMP	I	Thermal sensor connect terminal
15	TEMPR	O	Thermal sensor standard voltage output
16	SWDT	I	Serial data input for CXD2650R
17	SCLK	I	Serial clock input for CXD2650R
18	XLAT	I	Latch signal input for CXD2650R (at L: latch)
19	XSTBY	I	Standard signal input (at L: standby)
20	FOCNT	I	Internal circuit for CXD2650R (BFF22, BPF3T, EQ internal frequency control voltage input)
21	VREF	O	Standard voltage output (not used)
22	EQADJ	I/O	Internal frequency setting terminal for EQ Internal circuit
23	3TADJ	I/O	Internal frequency setting terminal for BPF3T Internal circuit
24	Vcc	-	Power supply +3V.
25	WBLADJ	I/O	Internal frequency setting terminal for BPF22 internal circuit
26	TE	O	Tracing error signal output for CXD2650R
27	CSLE	-	Sled error signal LPF external condenser connect terminal
28	SE	O	Sled error signal output for CXD2650R
29	ADFM	O	FM signal output for ADIP
30	ADIN	I	ADIP signal comparator input.
31	ADAGC	-	Capacitor connection terminal for ADIP
32	ADFG	O	ADDIP double signal output for CXD2650R
33	AUX	O	I3 signal/Thermal signal output for CXD2650R
34	FE	O	Focus error signal output for CXD2650R
35	ABCD	O	Optical signal output for CXD2650R
36	BOTM	O	RF/ABCD bottom hold signal output for CXD2650R
37	PEAK	O	RF/ABCD peak hold signal output for CXD2650R
38	RF	O	RF equalizer output for CXD2650R
39	RFAGC	-	Capacitor connection terminal for RF AGC circuit
40	AGCI	I	Input for RF AGC circuit
41	COMPO	O	Output for user comparator (not used)
42	COMPP	I	Input for user comparator (at L)
43	ADDC	I/O	Low frequency cut off
44	OPO	O	User ope-amp output (not used)
45	OPN	I	User ope-amp invert output (at L)
46	RFO	O	RF amp output
47	MORFI	I	Input for RF signal group
48	MORFO	O	RF signal output for group

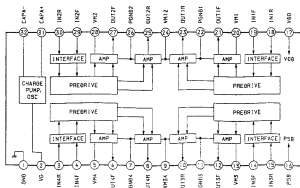
APC : Auto Power Control

AGC: AutoGain Control

IC121 CXD2650R (ENCODER / DECODER, ATRAC)



IC152 BH65111FS (SLED / SPRINDLE MOTOR DRIVE, FORCOS / TRACKING COIL DRIVE)



IC121 CXD2650R (1/2)

PIN NO.	PIN NAME	I/O	FUNCTION
1	MNT0 (FOK)	O	POK signal output from system control
2	MNT1 (SHCK)	O	Track jump detect signal output from system control
3	MNT2 (XBUSY)	O	Monitor 2 output from system control
4	MNT3 (SLOC)	O	Monitor 3 output from system control
5	SWDT	I	Write data signal input to system control
6	SCLK	I (S)	Serial clock signal input from system control
7	XLAT	I (S)	Serial latch signal input from system control
8	SRDT	O (3)	Write data signal output to system control
9	SENS	O (3)	Interruption status (SENSE) output to system control
10	XRST	I (S)	Reset input signal from system control
11	SQSY	O	Sub-cord Q sync. (SCOR) output to system control
12	DQSY	O	Digital in U-bit CD format sub-cord to system control
13	RECP	I	Laser power select input from system control
14	XINT	O	Interruption status output to system control
15	TX	I	Recording data output request input from system control
16	OSCI	I	System clock input (512Fs=22.5792MHz)
17	OSCO	O	System clock output (512Fs=22.5792MHz) (not used)
18	XTSL	I	System clock frequency setting terminal. "L": 45.1584MHz "H": 22.5792MHz (at "H")
19	DVDD	-	Digital section power supply (+3V)
20	DVSS	-	Digital section GND
21	DIN	I	Digital audio input (Optical input)
22	DOU	O	Digital audio output (Optical output)
23	ADDT	I	Input data from A/D converter
24	DADT	O	Data output to D/A converter
25	LRCK	O	A/D, D/A converter LR clock output (44.1kHz)
26	XBCK	O	A/D, D/A converter bit clock output (2.8224MHz)
27	FS256	O	11.2896MHz clock output (not used)
28	DVDD	-	Digital power supply (+3.0V)
29~32	A03 ~ A00	O	DRAM addres output (not used)
33	A10	O	DRAM addres output (not used)
34~38	A04 ~ A08	O	DRAM addres output (not used)
39	A11	O	DRAM addres output (not used)
40	DVSS	-	Digital section GND
41	XOE	O	DRAM output enable output (not used)
42	XCAS	O	DRAM CAS signal output (not used)
43	A09	O	DRAM addres output (not used)
44	XRAS	O	DRAM RAS signal output (not used)
45	XWE	O	DRAM write enable signal output (not used)
46	D1	I/O	DRAM data input/output (not used)
47	D0	I/O	DRAM data input/output (not used)
48,49	D2,D3	I/O	DRAM data input/output (not used)
50	MVCI	I (S)	Clock input from external VCO (connect to "L")
51	ASYO	O	Playback EFM dual signal output
52	ASYI	I (A)	Playback EFM compactor slice level input
53	AVDD	-	Analog section power supply (+3V)
54	BIAS	I (A)	Playback EFM compactor bias current input
55	RFI	I (A)	Playback EFM RF signal input
56	AVSS	-	Analog section GND
57	PDO	O (3)	Clock playback analog PLL plus compactor output of Playback EFM (not used)
58	PCO	O (3)	Rec/playback EFM master PLL plus compactor output
59	FILI	I (A)	Rec./playback section master PLL filter input
60	FILO	O (A)	Rec./playback section master PLL filter output
61	CLTV	I (A)	Rec./playback section master PLL inside VCO control voltage input
62	PEAK	I (A)	Optical signal peak hold input from CXA2523R
63	BOTM	I (A)	Optical signal bottom hold input from CXA2524R
64	ABCD	I (A)	Optical signal input from CXA2523R
65	FE	I (A)	Focus error signal input from CXA2523R
66	AUX1	I (A)	AUX A/D input

IC121 CXD2650R (2/2)

PIN NO.	PIN NAME	I/O	FUNCTION
67	VC	I (A)	Center voltage (+1.5V) input from CXA2523R
68	ADIO	O (A)	Monitor output of A/D converter input signal (not used)
69	AVDD	-	Analog section power supply (+3V)
70	ADRT	I (A)	A/D converter operation range upper limit voltage input (connect to "H")
71	ADRB	I (A)	A/D converter operation range lower limit voltage input (connect to "L")
72	AVSS	-	Analog section GND
73	SE	I (A)	Sled error signal input from CXA2523R
74	TE	I (A)	Tracking error signal input from CXA2523R
75	AUX2	I (A)	AUX A/D input (connect to "L")
76	DCHG	I (A)	Connect to power supply (+3V)
77	APC	I (A)	Laser digital APC error signal input (connect to "L")
78	ADFG	I (S)	ADIP dual FM signal input from CXA2523R (22.05 ± 1kHz)
79	F0CNT	O	Filter fo control output from CXA2523R
80	XLRF	O	Control lurch output from CXA2523R
81	CKRF	O	Control clock output from CXA2523R
82	DTRF	O	Control data output from CXA2523R
83	APC REF	O	Laser APC references PWM output
84	LDDR	O	Laser digital APC PWM output (not used)
85	TRDR	O	Tracking servo drive PWM output (-)

Q701 MB-M3 (MECHANISM CONTROL MICROPROCESSOR) (1/2)

PIN NO.	SYMBOL	I/O	DESCRIPTION
1	NC		Open
2	NC		Open
3		I	Connect to "H"
4		I	Connect to "H"
5		I	Connect to "H"
6	CHUCKIN SW	I	Detect input for chucking switch (at detect: "L")
7	NC		
8	PACK OUT SW	I	Sensor input for Back out switch (at detect: "L")
9			connect to "L"
10			connect to "L"
11	AVSS		connect to "L"
12	XINT	I	Interrupt stage input for IC121
13	P.DOWN	I	Power down detect input (at normal: "H")
14			connect to "L"
15	SQSY	I	Sub-cord Q sync. input
16	DQSY	I	Digital input U-bit. Sub-Q sync. input for CD format
17			connect to "L"
18			connect to "L"
19			connect to "L"
20	SYS_RST	I	System reset signal input
21	TEST	I	GND connect.
22	+3.3V		Power supply input +3.3V.
23	VBAT	I	Back up power supply terminal
24	X.OUT		Clock output (32kHz)
25	X.IN		Clock input (32kHz)
26	GND		GND terminal
27	X. OUT		Clock output (12MHz)
28	X.IN		Clock input (12MHz)
29	GND		GND terminal
30	NC		Open
31			connect to "L"
32	SENS	I	Internal status input for IC121
33	SHOCK	I	Shock detector signal input for IC121
34			connect to "L"
35			connect to "L"
36	STB	O	Strobe signal output for power supply circuit
37	REC-P	I	Detect input for rec position switch. (at rec: "L")
38	PB-P	I	Detect input for PB position switch. (at PB: "L")
39	LDLOW	O	Loading speed select signal output
40	NC		open
41	MNT2	I	Internal status input for IC121
42	MNT3	I	Internal status input for IC121
43	NC		open
44			connect to "L"
45			connect to "L"
46	NC		Open
47	GND		GND
48	+3.3V		Power supply input (+3.3V)
49			connect to "L"
50			connect to "L"
51			connect to "L"
52	SDA	I/O	Data signal input for IC171
53	SCL	O	Clock signal output for IC171
54			connect to "L"
55			connect to "L"
56			connect to "L"
57	RXD	I	RS232C terminal
58	TXD	O	RS232C terminal
59	RTS	O	RS232C terminal
60	CTS	I	RS232C terminal
61			connect to "L"
62			connect to "L"
63			connect to "L"
64			connect to "L"

Q701 MB-M3 (MECHANISM CONTROL MICROPROCESSOR) (2/2)

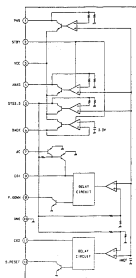
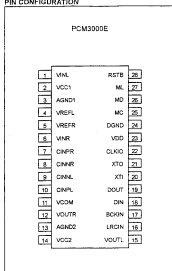
PIN NO.	SYMBOL	I/O	DESCRIPTION
65	GND		GND terminal
66	+3.3V		Power supply input (+3.3V)
67	SCLK	I/O	Clock signal output to serial bus
68	SWDT	I/O	Write data signal output to serial bus
69	SRDT	I	Read data signal input from serial bus
70	NC		
71	NC		Open
72	NC		Open
73	NC		Open
74			at "H"
75	LDON	O	Laser on/off control output. (at on: "H")
76	NC		Open
77	FOK	I	FOK signal output from IC121
78	NC		Open
79	NC		Open
80	WRPWR	O	Laser power select signal output to Optical block
81	DIG-RST	O	Digital section reset signal output
82	NC		Open
83	DA-RST	O	Reset signal output to DA converter
84	NC		Open
85	NC		Open
86	MOD	O	Laser modulation select signal output
87	NC		Open
88	NC		Open
89	SCTX	O	Write data transmit timing output to IC121
90	XLATCH	O	Latch signal output to serial bus
91	NC		Open
92	NC		Open
93	AMUTE	O	Line output mute output (at "L": mute)
94	LDOUT	O	Loading motor control output
95	LDIN	O	Loading motor control output
96	LIMITIN	I	Detect input from limit in switch (at "L": limit in)
97	PROTECT	I	Detect input from protect switch (at "H": protect)
98	REFLECT	I	Detect input from reflect switch (at "H": low reflect)
99	GND		GND
100	+3.3V		Power supply input +3.3V.

Q301 PCM3000E (D/A CONVERTER)

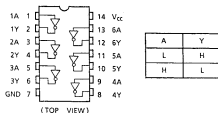
PIN ASSIGNMENTS

PIN	NAME	IO	DESCRIPTION
1	VINL	IN	ADC Analog Input, Lch
2	VCC1	-	ADC Analog Power Supply
3	AGND1	-	ADC Analog Ground
4	VREFL	-	ADC Input Reference, Lch
5	VREFR	-	ADC Input Reference, Rch
6	VINR	IN	ADC Analog Input, Rch
7	CINPR	-	ADC Anti-alias Filter Capacitor(+), Rch
8	CINNR	-	ADC Anti-alias Filter Capacitor(-), Rch
9	CINNL	-	ADC Anti-alias Filter Capacitor(+), Lch
10	CINPL	-	ADC Anti-alias Filter Capacitor(-), Lch
11	VCOM	-	DAC Output Common
12	VOUTR	OUT	DAC Analog Output, Rch
13	AGND2	-	DAC Analog Ground
14	VCC2	-	DAC Analog Power Supply
15	VOUTL	OUT	DAC Analog Output, Lch
16	LRCIN	IN	Sample Rate Clock Input (fs)
17	BCKIN	IN	Bit Clock Input
18	DIN	IN	Data Input
19	DOUT	OUT	Data Output
20	XTI	IN	Oscillator Input
21	XTO	OUT	Oscillator Output
22	CLKO	IO	Buffered Output of Oscillator or External Clock Input
23	VDD	-	Digital Power Supply
24	DGND	-	Digital Ground
25	MC	IN	Mode Control for Serial Bit Clock ^(*)
26	MD	IN	Mode Control for Serial Data ^(*)
27	ML	IN	Mode Control for Strobe Pulse ^(*)
28	RSTB	IN	Reset ^(*)

PIN CONFIGURATION

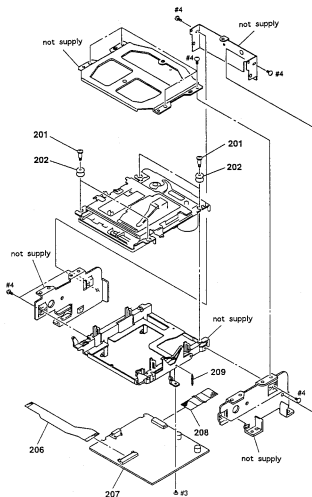


Q951 LA5620 (VOLTAGE REGULATOR)

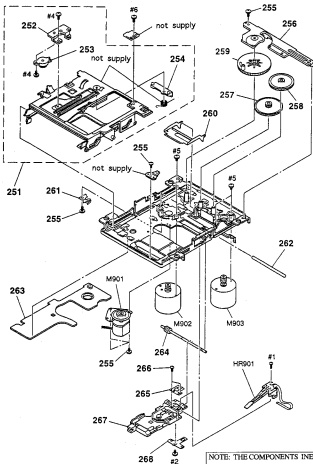


Q503 74HC00

MD MECHANISM EXPLODED VIEW



REF. NO.	PART NO.	DESCRIPTION	REF. NO.	PART NO.	DESCRIPTION
201	4628-167-01	Screw	207	A4699-615-A	BD pc board ass'y
202	4987-327-01	Insulator	208	1777-517-11	Flat cable (15 wires)
206	1660-966-11	Flexible flat cable, Optical pick-up	209	4987-910-01	Spring coil



NOTE: THE COMPONENTS IDENTIFIED BY MARK Δ ARE CRITICAL FOR RISK OF FIRE AND ELECTRIC SHOCK. REPLACE ONLY WITH PART NUMBER SPECIFIED.

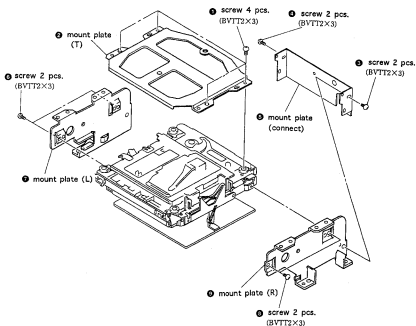
REF. NO.	PART NO.	DESCRIPTION
251	A4672-138-A	Slider ass'y
252	4983-439-01	Bracket (Dumper)
253	3953-235-01	Oil damper
254	4983-437-01	Slider (cam)
255	3342-375-11	Special screw (M1.7X1.4)
256	4979-490-31	Bracket (Gear)
257	4979-498-01	Gear (LB)
258	4979-499-01	Gear (LC)
259	4979-497-21	Gear (LA)
260	4979-485-01	Lever (Head up)
261	4979-906-01	Leaf spring (load screw)

REF. NO.	PART NO.	DESCRIPTION
262	4984-556-01	Shaft (main shaft)
263	1661-774-11	SW pc board
264	A3304-200-A	Lead screw ass'y
265	4963-914-02	Lock (asenter)
266	3366-890-11	nylon lock (M1.4)
267	△ 8533-028-01	Optical pick-up (KMS-260A/JTN)
268	4973-061-01	Spacer (luck)
HR001	1500-396-11	Over write head
M902	A4672-135-A	Spindle motor ass'y
M902	A4672-133-A	Slide motor ass'y
M903	A4672-134-A	Load motor ass'y

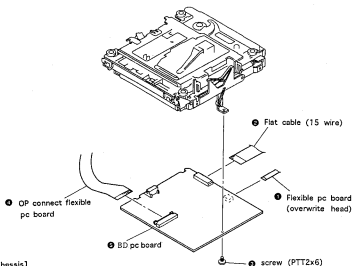
MD MECHANISM DISASSEMBLY

- Remove the parts in numerical order.

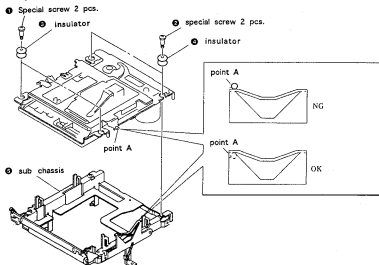
[mount plate (T),(L),(R)]



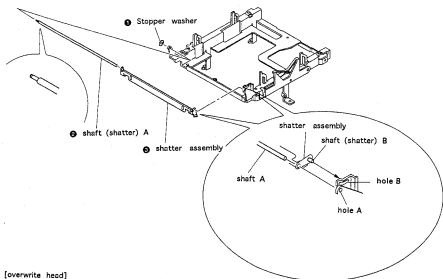
[DB pc board]



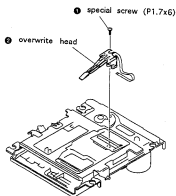
[Sub chassis]



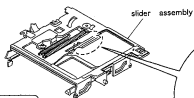
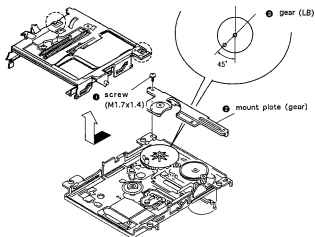
[Shutter assembly]



[overwrite head]



[slider assembly]



OK



NG



1. TEST MODE

1.1 Precaution for Using the Test Mode

1. In the test mode, the loading related movement does not correspond with the test operation.
Be sure that the disc is stopped completely before replacing the disc.
Pressing the EJECT key when a disc is rotating in continuous play/recording, etc. does not stop the rotation of the disc, causing the rotating disc to be ejected.
Be sure to press the EJECT key while pressing the NO key.
2. In the test mode, the open/close state of the record-protect tab is not detected and therefore, if the unit enters a laser power emitting mode such as continuous recording mode (CREC MODE) and traverse adjustment mode (EFBAL ADJUST), the recorded conte

1.2 Setting the Test Mode

1. Connect the pin 69 of Q702 (M38067MC-241FP) and GND.
2. Plug in the power cord of the unit into a wall outlet.

1.3 Canceling the Test Mode

Unplug the power cord of the unit from the wall outlet.

1.4 Basic Operation in the Test Mode

In the test mode, all operations are made through three controls: AMCS knob, YES key and NO key. The functions of these controls are shown below.

Function name	Function
AMCS knob	Changes the parameter and test item.
YES key	Proceeds to the next step or finalizes the operation.
NO key	Returns to the previous step or aborts the operation.

1.5 Selecting the Test Item

Select the desired test item from the following eight items by turning the AMCS knob.

Display information	Test item
TEMP ADJUST	Temperature compensation offset adjustment
LDPWR ADJUST	Laser power adjustment
LDPWR CHECK	Laser power adjustment
EFBAL ADJUST	Traverse adjustment
FBIAS ADJUST	Focus bias adjustment
FBIAS CHECK	Focus bias adjustment
CPLAY MODE	Continuous play mode
CREC MODE	Continuous recording mode

For details of individual test items, see the applicable section in "2. Electrical Adjustments".

If you select a wrong test item, press the NO key to deselect the item.

* EEP MODE (non-volatile memory mode) is not used for servicing.

If you select this item by mistake, press the NO key immediately to deselect the item.

1.6 Functions of Other Controls

Control name	Function
	Causes continuous playing when stopped; Turns ON/OFF the tracking servo when in continuous play.
	Stops continuous playing/recording.
	Moves the thread to outside while the key is pressed.
	Moves the thread to inside while the key is pressed.
	Starts/stops recording during continuous play.
PLAY MODE	Switches spindle servo mode (between "CLV" and "A") each time the key is pressed.
DISPLAY	Switches display mode each time the key is pressed.

Note: The open/close state of the record-protect tab will not be detected during the test mode.

Remember that pressing the  (REC) key erases the recorded contents regardless of the position of the record-protect tab.

1.7 Meanings of Other Display Information

Display information	Mode		
	Lit	Not Lit	Flashing
	In continuous play	Stopped	
	Tracking servo OFF	Tracking servo ON	
REC	Recording mode ON	Recording mode OFF	
CLOCK	CLV LOCK	CLV UNLOCK	
TRACK	Bit	Group	
DISC	High reflection	Low reflection	
DATE	CLV-S	CLV-A	
A.SPACE	ABCD adjustment completed		
A-B	Focus auto gain successful		Focus auto gain successful
	Tracking auto gain successful		Tracking auto gain failed

2. ELECTRICAL ADJUSTMENTS

2.1 Precautions for Checking Laser Emission from the Laser Diode

When checking the emission of laser from the laser diode during adjustments, never if from directly above the laser diode. Doing so may cause loss of your eyesight.

2.2 Precautions for Handling the Optical Pickup (KMS-260A)

The laser diode inside the optical pickup is easily damaged by static electricity.

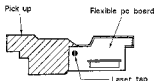
When handling the optical pickup, solder-bridge the laser tap located on the flexible circuit board.

If you are disconnecting the optical pickup, provide solder-bridging before removing it;

Do not remove the solder bridging before reconnecting the optical pickup.

Also, take sufficient preventive measures against static electricity when working on it.

It is also noted that the flexible circuit board must be handled carefully because its wiring is easily broken.



2.3 Precautions for Adjustments

- 1) Whenever you have replaced the optical pickup, adjust the laser power.
- 2) Conduct adjustments in the test mode.
Exit from the test mode if you have finished adjustments.
- 3) Use the following test disc and measuring instruments.

CD test disc: TGYS-1

Laser power meter: LPM-8001 (manufactured by LEADER)

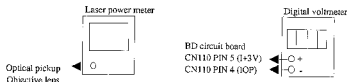
Oscilloscope (with 40MHz band or more; Conduct probe CAL. before measurement.)

Digital voltmeter

- 4) If you monitor two or more signals on an oscilloscope, do not connect VC to GND inside the oscilloscope (Otherwise, short-circuit will occur between VC and GND.)

2.4 Adjusting the Laser Power

Connection:



Adjustment:

1. Install the laser power meter on the objective lens of the laser pickup.
(If it cannot be installed properly, move the pickup using the <-<- or ->> key.)
Connect the digital voltmeter to CN110 Pin 5 (I+3V) and CN110 pin 4 (IOP).
2. Turn the AMCS knob until "LDPWR ADJUS" is displayed.
(Laser power: For adjustment)
3. Press the YES key to display "LD 0.9mW S□□".
4. Adjust AMCS knob such that the laser power reading becomes 0.86mW~0.92mW.
Press the YES key to display "LD SAVE S□□".
5. Press the YES key to display "LD 7.0mW S□□".
6. Adjust AMCS knob such that the laser power reading becomes 6.9mW~7.1mW.
Press the YES key to display "LD SAVE S□□".
7. Turn the AMCS knob until "LDPWR CHECK" is displayed.
8. Press the YES key to display "LD 0.9mW S□□".
Laser power meter reading: 0.85mW ~ 0.91mW

9. Press the YES key to display "LD 7.0mW S□□".

Verify that the readings on the laser power meter and the digital voltmeter are within the value specified below:

Standard value:

Laser power meter reading: 7.0mW ± 0.1mW

Digital voltmeter reading: Optical pickup displayed value ± 10%
(Optical pickup label)



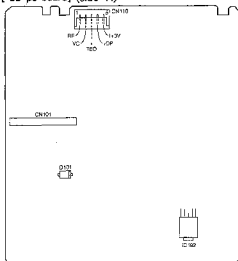
In this case, $I_{op}=82.5\text{mA}$

$I_{op} \text{ (mA)} = \text{Digital voltmeter reading (mV)} / 1 \text{ (}\Omega\text{)}$

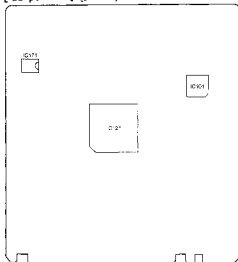
10. Press the NO key to display "LDPWR CHECK", then stop the laser emission.
(The NO key is always accepted to stop the laser emission.)

2.5 Adjustment point

[BD pc board] (side A)



[BD pc board] (side B)



Message list

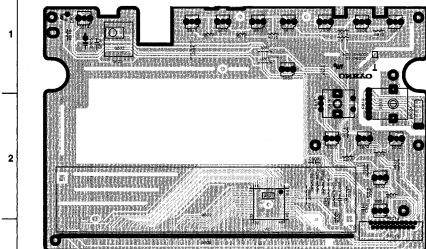
The following table explains the various messages that appear in the display.

Message	Meaning
All Erase!!	An attempt was made to record onto a faulty MD.
Blank Disc	A new (blank) or erased disc has been inserted.
Cannot Copy	An attempt was made to make a second copy from a digitally dubbed MD (see "Rules for Digital Recording" on page 30).
Cannot Edit	An attempt was made to edit the disc during PRR-GRAM or RANDOM play.
Cannot Rec	An attempt was made to record onto a preformatted (read-only) disc.
CD Dub Fail	The Pause button was pressed during CD-dubbing. (The Pause button, which is effective for normal recording operation, is not usable during CD dubbing.)
D. In Unlock	The digital equipment (CD player, DAT, etc.) has not been connected properly. Otherwise, the connected digital equipment is not operating properly.
Disc Error	The disc is abnormal (scratched or missing a TOC).
Disc Full	The disc is full (see "System Limitations" on this page).
Impossible	An attempt was made to combine the first track.
Name Full	The labeling capacity of the disc has reached its limit.
No Change	The naming attempt failed.
No Disc	There is no disc in the unit.
No Track	The inserted disc has a disc time but no tracks.
Over	In pause mode (when playing is paused), the FF (Fast Forward) button was pressed to the end of the disc.
Program Full	An attempt was made to record a 26th track.
Protected	The inserted disc is record-protected.
Retry	The first recording attempt failed due to a disturbance or scratch on the MD, and a second recording is being made.
Retry Error	The recording attempt failed due to a consecutive disturbance or scratches on the MD.
Sorry	An attempt was made to combine tracks that cannot be combined.
TOC Error	The reading of the TOC failed.
U-TOC Error	The writing of the recorded or edited contents failed.

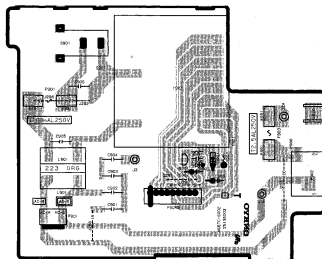
Even if "Disc Error" is displayed, you may be able to record on the disc. Press the **●** REC button and, when "All Erase!!" appears in the display, try pressing the **●** RTIC button again.

A B C D E F G

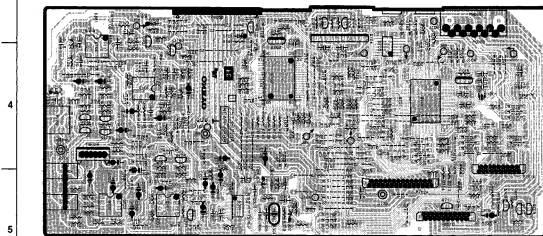
PRINTED CIRCUIT BOARD BOTTOM VIEWS



U4 DISPLAY CIRCUIT PC BOARD



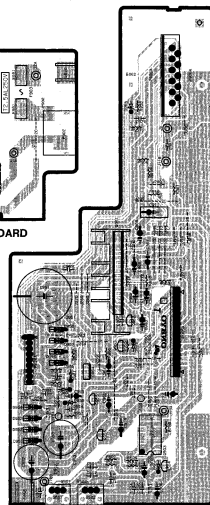
U2 POWER TRANSFORMER CIRCUIT PC BOARD



U1 DIGITAL CIRCUIT PC BOARD



U5 AC OUTLET PC BOARD

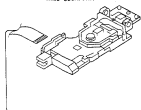


U3 POWER SUPPLY CIRCUIT PC BOARD

A B C D E F G

PRINTED CIRCUIT BOARD (BD SECTION)

1

OPTICAL PICK-UP BLOCK
(KMS-280A/27N)

2

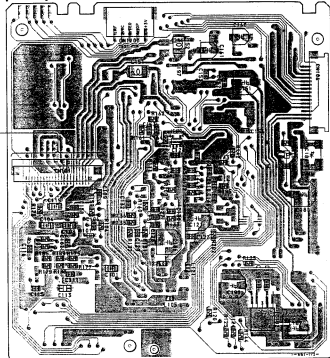
3

W001
OVER WHITE HEAD

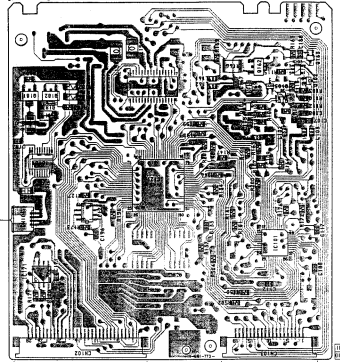
4

5

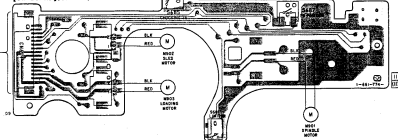
[BD BOARD](SIDE A)



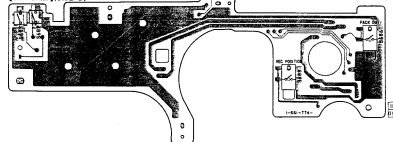
[BD BOARD](SIDE B)

② MAIN BOARD
CX334③ MAIN BOARD
CX335

[SW BOARD](SIDE A)



[SW BOARD](SIDE B)



PRINTED CIRCUIT BOARD PARTS LIST

U1 DIGITAL CIRCUIT PC BOARD (NAAR-9999-1B/1C)

CIRCUIT NO.	PART NO.	DESCRIPTION
ICs		
Q101, Q102, D201, Q302	22240191	NM48565D-D
Q301	22241078	PCMC3000E
Q701	2224084K3	MR-M3
Q702	22241080K3	M38067MC-241FP
Q703	22241081R2	1C74VHC704
Q704	22240848	LB1641
Transistors		
Q103-Q106, Q709, Q710	2211705 or 2211706	2SD655-B or 2SD655-F
Q107, Q108, Q205, Q707, Q711	2213590 or 2213090	RN2207 or DTA114YS
Q203, Q204	2213600 or 221299	RN1211 or DTC114TS
Q705	2213570 or 221281	RN1207 or DTC114YS
Q706	2213105 or 2213106	2SA1115-E or 2SA1115-F
Q708	2213074 or 2213075	2SA933-R or 2SA933-S
Diodes		
D701	224470362	MTZJ5.6B, Zener
D702	223222 or 223163 or 223205	W0713A or 1SS133 or 1SS270A
Capacitors		
C101, C102, C109, C110, C114, C210, C202, C209, C210, C309, C310, C313	393341007	10 μ F, 16V, Electro.
C105, C104-C106	393341007	10 μ F, 16V, Electro.
C107, C108	374721524	1500pF $\pm$ 5%, 50V, Plastic
C111, C112	374724724	4700pF $\pm$ 5%, 50V, Plastic
C115, C116	393341017	100 μ F, 16V, Electro.
C205-C208	374723915	390pF $\pm$ 10%, 50V, Plastic
C308	393341017	100 μ F, 16V, Electro.
C311, C312	393224707	47 μ F, 6.3V, Electro.
C313	374724714	4700pF $\pm$ 5%, 50V, Plastic
C703	393380477	4.7 μ F, 50V, Electro.
C704	374723344	0.33 μ F $\pm$ 5%, 50V, Plastic
C709	374721044	0.1 μ F $\pm$ 5%, 50V, Plastic
C709	393322217	220 μ F, 6.3V, Electro.
Coils		
L301, L302	230906	RL62RN2-R62
Jacks		
J703	25045330 or 25045481	NPT-2PDBL184 or NPT-2PDBL299, RI
Pin Jack		
P201	25045397	NPJ-4PDWR222
Sockets		
F701A	25051771	NSCT-19P1558
F702A	25051781	NSCT-20P1568
F801B	25051773	NSCT-21P1560
F906B	25051051	NSCT-11P818
Plugs		
P802B	25051510	NPLG-64P134
P905B	25051530	NSCT-18P1317
Resistor		
X702	3010252	CS11-2.0MTW, Ceramic lock
X703	3010190	CS18-0.0MTW, Ceramic lock
Crystal		
X701	3010274	HC-49/UC3, 22.5792MHz
X701	3010275	DT-38(32KHz)

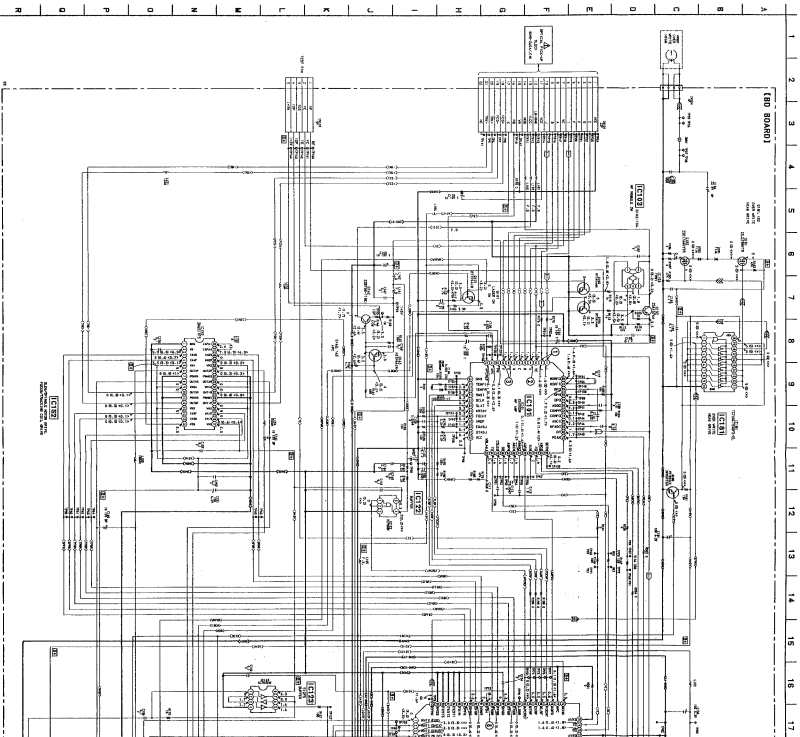
L2 POWER TRANSFORMER CIRCUIT PC BOARD

CIRCUIT NO.	PART NO.	DESCRIPTION
Transistors		
Q901	2213074 or 2213075	2SA933-R or 2SA933-S
Diodes		
D901	22380035 or 22380260	GP104003E or RL1N4003E
D902	224472704	MTZJ27D, Zener
Capacitors		
C906	3300191	0.01 μ F $\pm$ 20%, AC400V/1.25V, IS C
C907	393380477	4.7 μ F, 50V, Electro.
C909	393381017	100 μ F, 50V, Electro.
Resistor		
R902	443532204	230hm, 1/2W, $\pm$ 5%, Metal oxid
Coil		
L901	231252	NCH-3489
Fuse holder		
R881	25030065	YSH403T <P,PT>
AC outlet		
P902	25050684	Δ NSCT-2P488 <DT>
P903	25050410	Δ NSCT-2P235 <P,PT>
Plug		
P901	25055875	NPLG-2P631
Capacitor cover		
C906A	27301216	SB1925A

U3 POWER SUPPLY CIRCUIT PC BOARD

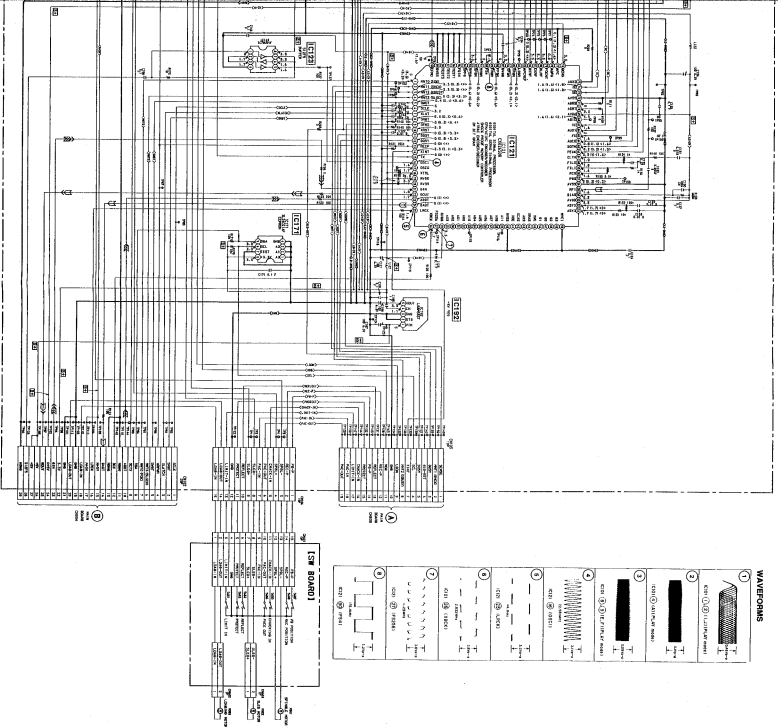
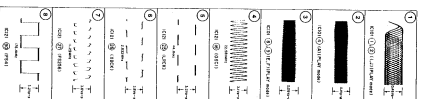
CIRCUIT NO.	PART NO.	DESCRIPTION
ICs		
Q503	222740005TOS	74HC00P
Q504	222780023	78L02
Q505	22241077	LA5620
Q506	222780055JRC	NM78M05FA
Q507	222780063	78L06
Q508	222790063	79L06
Transistors		
Q502	2213570 or 221281	RN1207 or DTC114YS
Q503	2213590 or 2213090	RN2207 or DTA114YS
Diodes		
D501, D560-D562	223222 or 223163 or 223205	W0713A or 1SS133 or 1SS270A
D501-D508	22380035 or 22380260	Δ GP104003E or RL1N4003E
D509	224470562	MTZJ5.6B, Zener
D563	224470623	MTZJ6.2C, Zener
Capacitors		
C501, C503	374721034	0.01 μ F $\pm$ 5%, 50V, Plastic
C505	374721044	0.1 μ F $\pm$ 5%, 50V, Plastic
C506	393324717	470 μ F, 6.3V, Electro.
C507	393331017	100 μ F, 6.3V, Electro.
C508	3300181	22000 μ F, 16V, Electro.
C502, C503	393343327	3300 μ F, 16V, Electro.
C505	393341007	10 μ F, 16V, Electro.
C506, C562, C507	374721044	0.1 μ F $\pm$ 5%, 50V, Plastic
C508	374722244	0.22 μ F $\pm$ 5%, 50V, Plastic
C509, C560	393344717	470 μ F, 16V, Electro.
C561, C562, C568	393322217	220 μ F, 6.3V, Electro.
C563	393380477	4.7 μ F, 50V, Electro.
C564	393380107	1 μ F, 50V, Electro.
C565	393380227	2.2 μ F, 50V, Electro.
C570	3900389	DX-383V105, super

SCHEMATIC DIAGRAM (1) BD SECTION

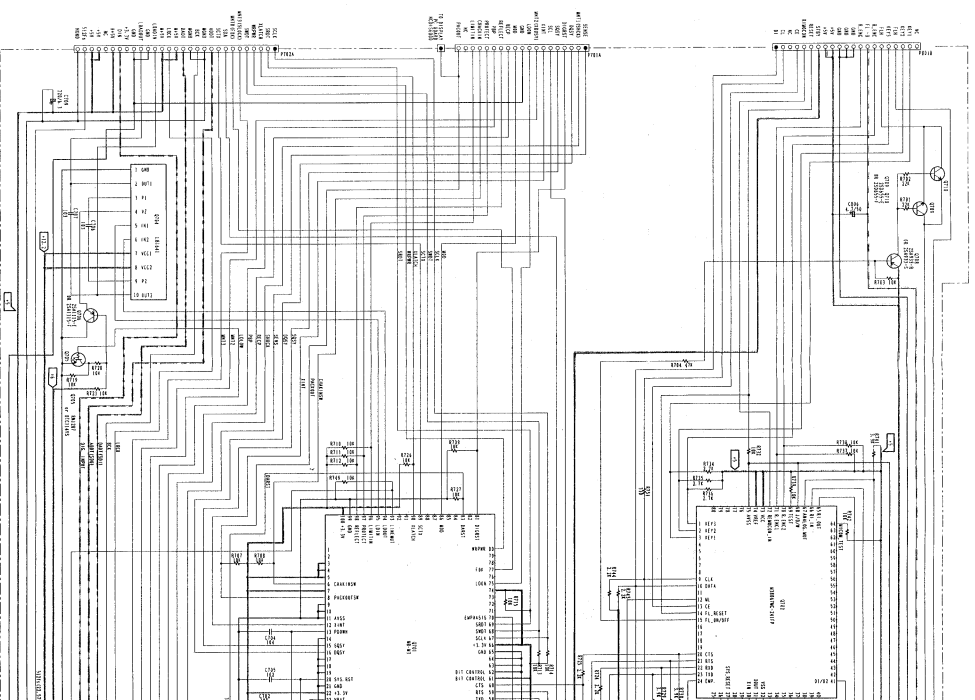


NOTE: THE COMPONENTS IDENTIFIED BY MARK
 Δ ARE CRITICAL FOR RISK OF FIRE AND
 ELECTRIC SHOCK. REPLACE ONLY WITH
 PART NUMBER SPECIFIED.

WAVEFORMS

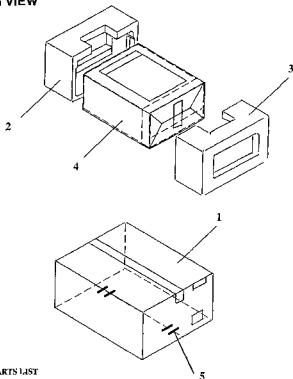


SCHEMATIC DIAGRAM (2)





PACKING VIEW



PARTS LIST

REF. NO.	PART NO.	DESCRIPTION
1	29053137	Carbot box <S>
	29053138	Carbot box
2	29091788A	Pad (F)
3	29091789A	Pad (B)
4	29100123C	Poly Bag 550x430
5	282321	Staple
	29342413	Instruction manual, E
	29342414	Instruction manual, U3(FSI) <P>
	29342415	Instruction manual, U3(GSwD) <P>
	29342416	Instruction manual, T <PT,DT>
	29355262	Instruction sheet, U8
	29355250	Instruction sheet, U8(NCS-1P)
	29100097-1AY	Poly Bag 320x250
	2010098A or	Cord AS
	2010326Y	
	2010200	3.5 mm Plug, cord AS
	2050036A	Cord AS, NCS-1P101
	24140301	Remote control, RC-301MD
	3010054	Battery, UM-3
	29362036	Label(LX)
	29361573	Label, (PE-LD)

NOTE: <S> Silver model only
 Black model only
 <P> 230V model only
 <PT> Asian model only
 <DT> Taiwanese model only

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